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NOTES:
1.HSF Property:Comply iSupplier system HSF property attribute up-to-date value.

CALTECH 11.6"
OXFORD 14"
(BSW)
CS2 BUILD
2015.5.15

21-OCT-2002		
DATE	CHANGE NO.	REV

DESIGN/DRAWER				DATE				21-OCT-2002			
CHECK											
APPROVAL											
FILE NAME				CALTECH OXFORD							
PCB PIN				XXXXXXXX				AX1			

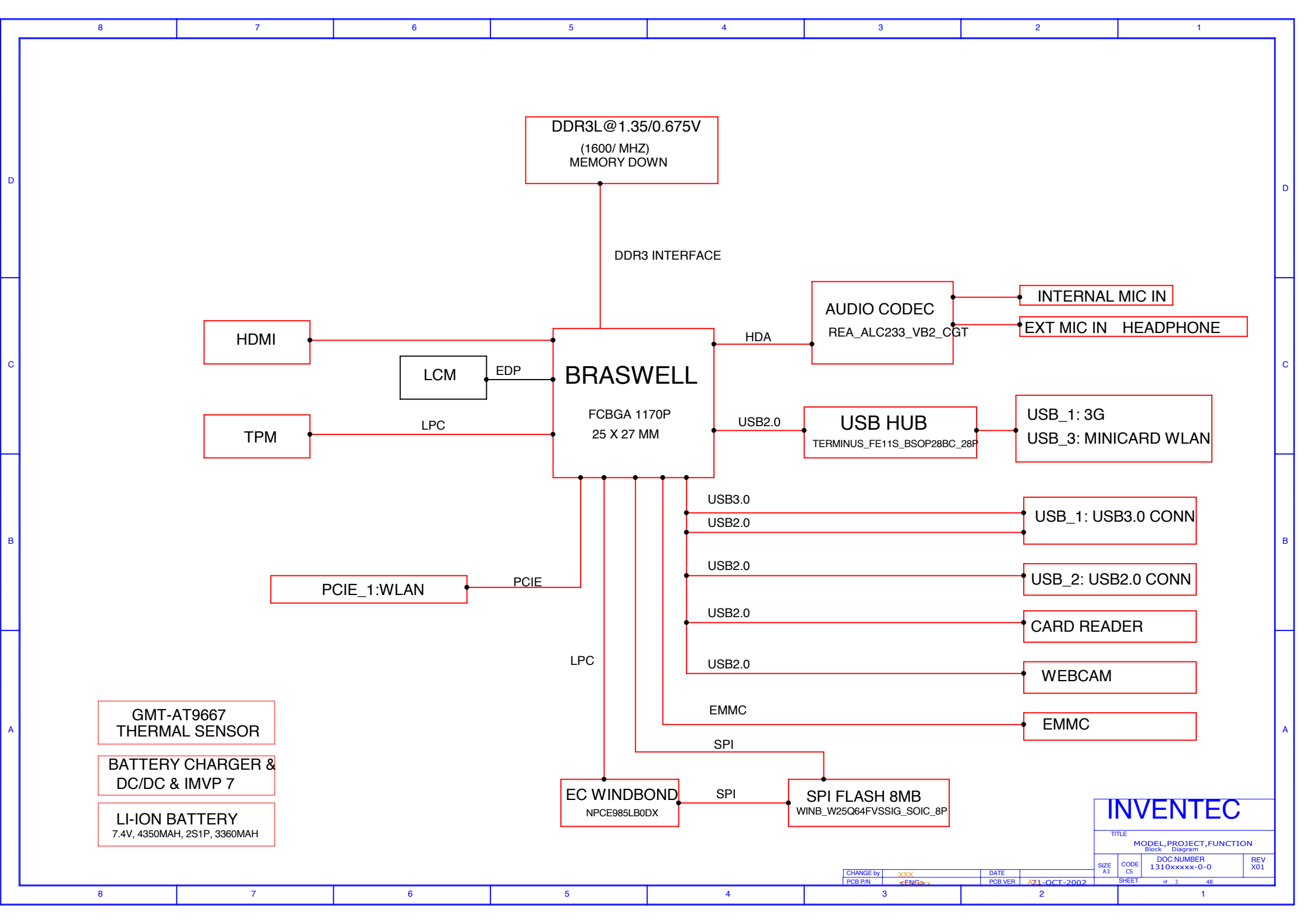
TITLE				MODEL,PROJECT,FUNCTION			
INVENTEC							
SIZE	CODE	DOC NUMBER	REV	SHEET	of	48	
A3	CS	1310XXXXXXXX-0-0	X01				

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7. POWER +V1.35S	44. USB2.0 HUB
8. POWER VCORE	45. EMI
9. POWER VCORE/MOS	46. USB2.0 DB
10. POWER PVGG	47. APP LED DB(NO COMPONENT)
11. POWER PVGG/MOS	48. PWRBUTTON DB
12. MOIC(1V8.1V05.1V5.1V24.1V15)	
13. ENABLE PIN	
14. LOAD SWITCH	
15. PCB SCREW	
16. KB&TOUCH PAD CONN	
17. EC	
18. AUDIO CODEC	
19. SPEAKER/HP JACK/MIC JACK	
20. CARDREADER	
21. NGFF WLAN/DEBUG CARD	
22. 3G(NO COMPONENT)	
23. USB2.0	
24. USB3.0	
25. EMMC	
26. POWER BOARD / TPM	

INVENTEC

CHANGE by	XXX	DATE	21-OCT-2002	SIZE	A3	CODE	CS	DOC NUMBER	1310xxxxx-0-0	REV	X01
PCB P/N	xxxxxxx	PCB VER	AX1	SHEET	2	of	48				



D

C

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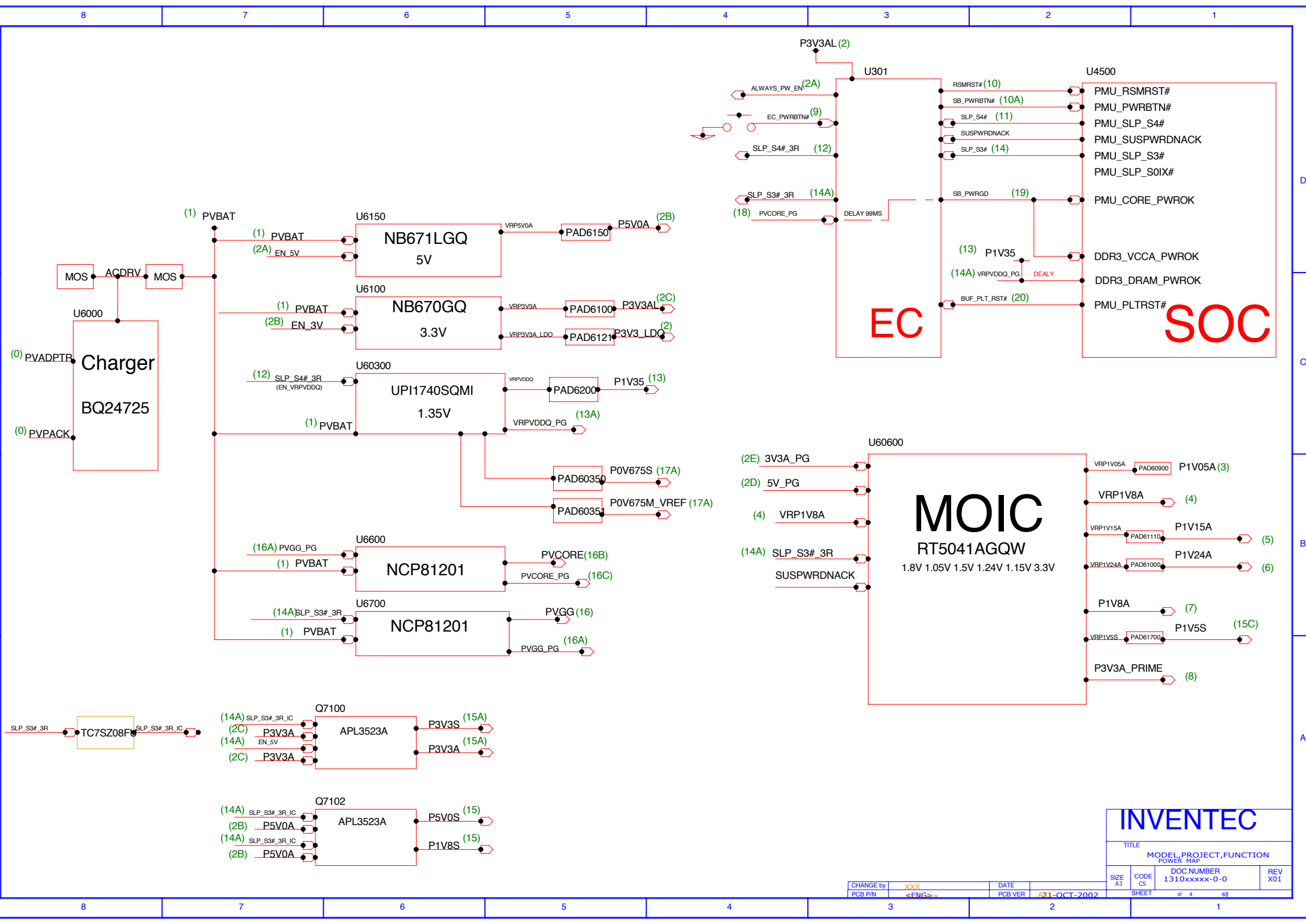
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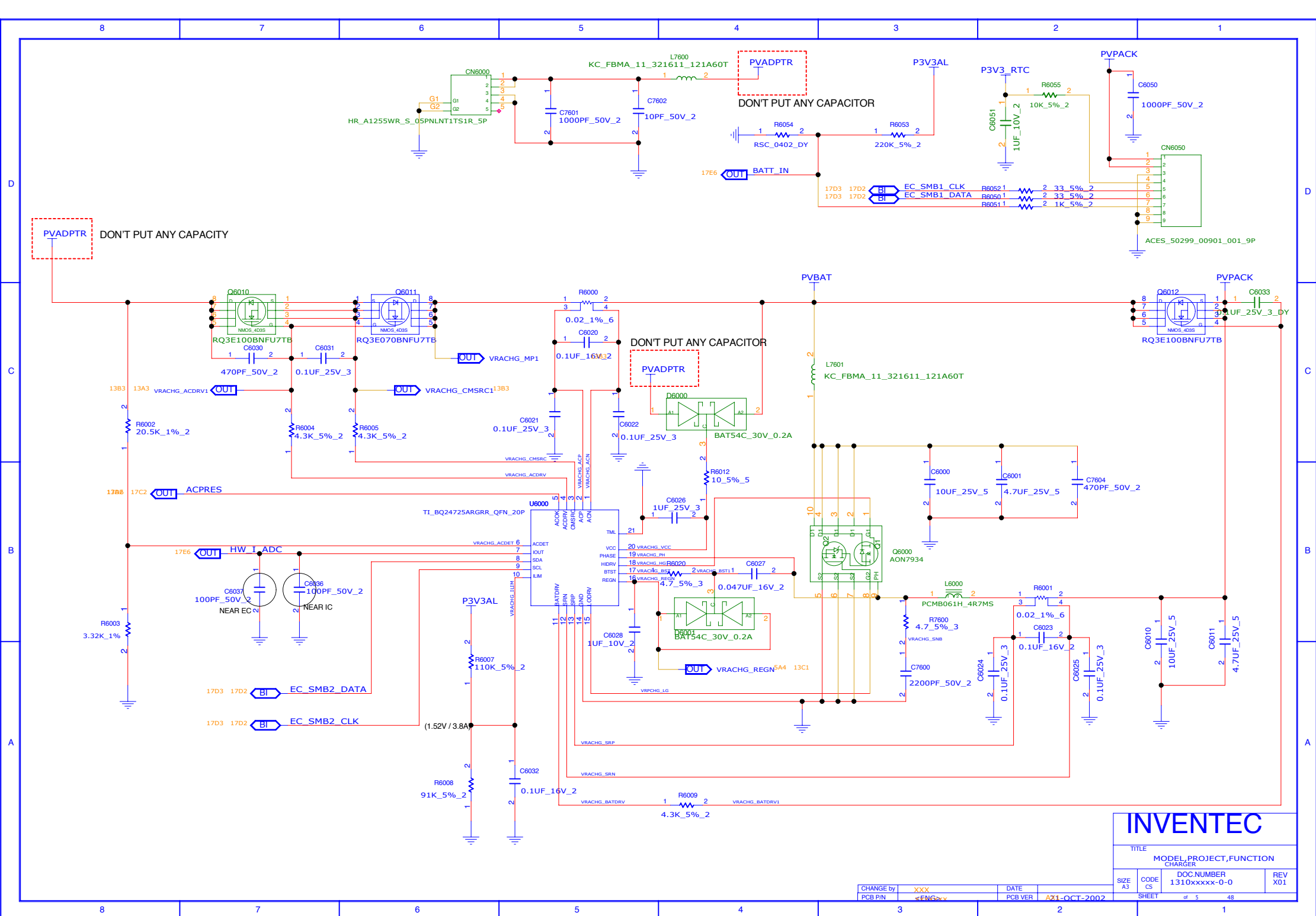
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A



CHANGE by XXX		DATE	
PCB P/N <ENG>		PCB VER	
		A71-OCT-2002	

INVENTEC			
TITLE			
MODEL PROJECT,FUNCTION			
POWER MAP			
SIZE A3	CODE CS	DOC NUMBER 1310xxxxx-0-0	REV X01
SHEET of 4		48	



D

C

B

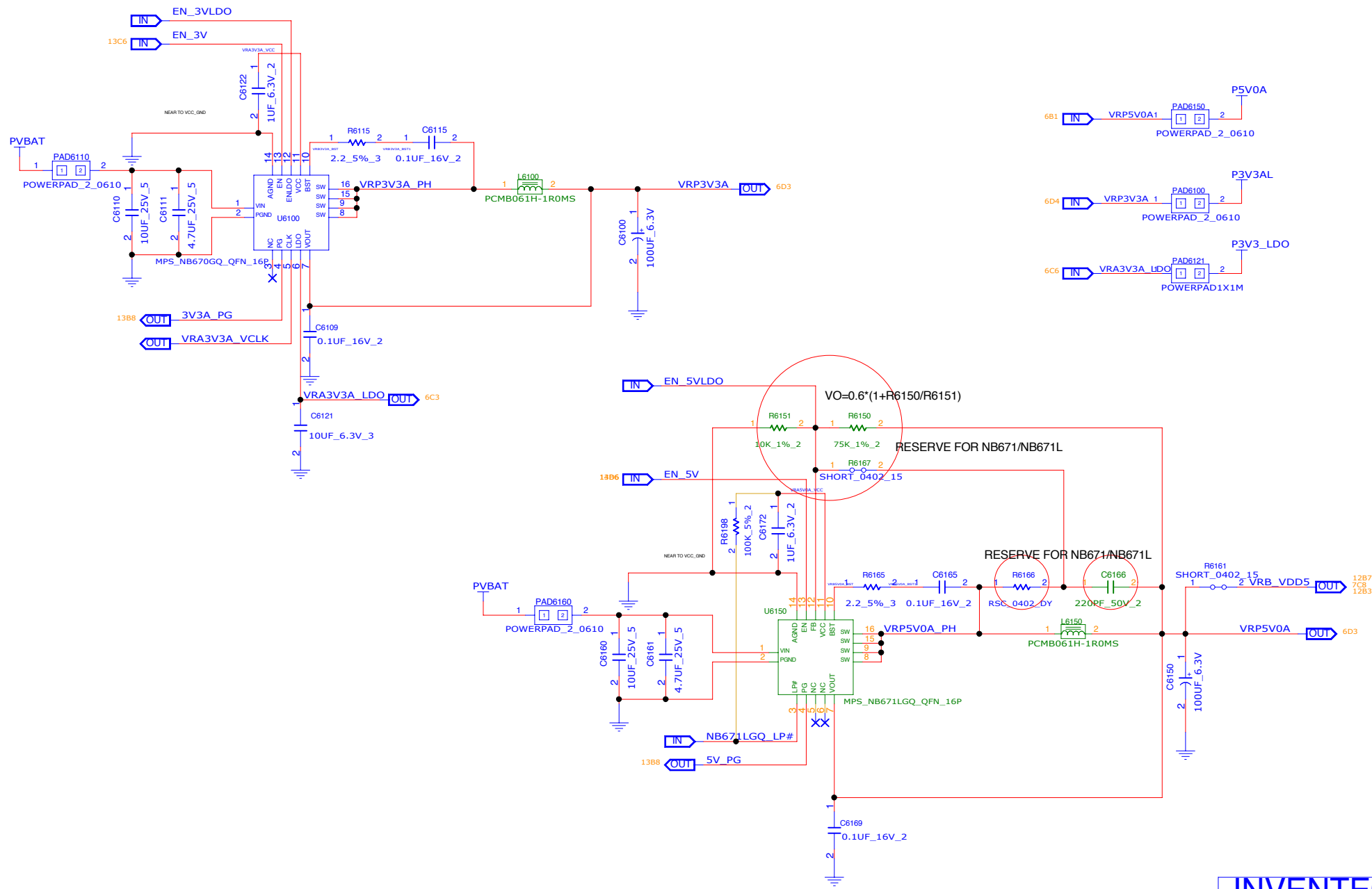
A

D

C

B

A



INVENTEC

TITLE

MODEL,PROJECT,FUNCTION

3V / 5V

SIZE A3

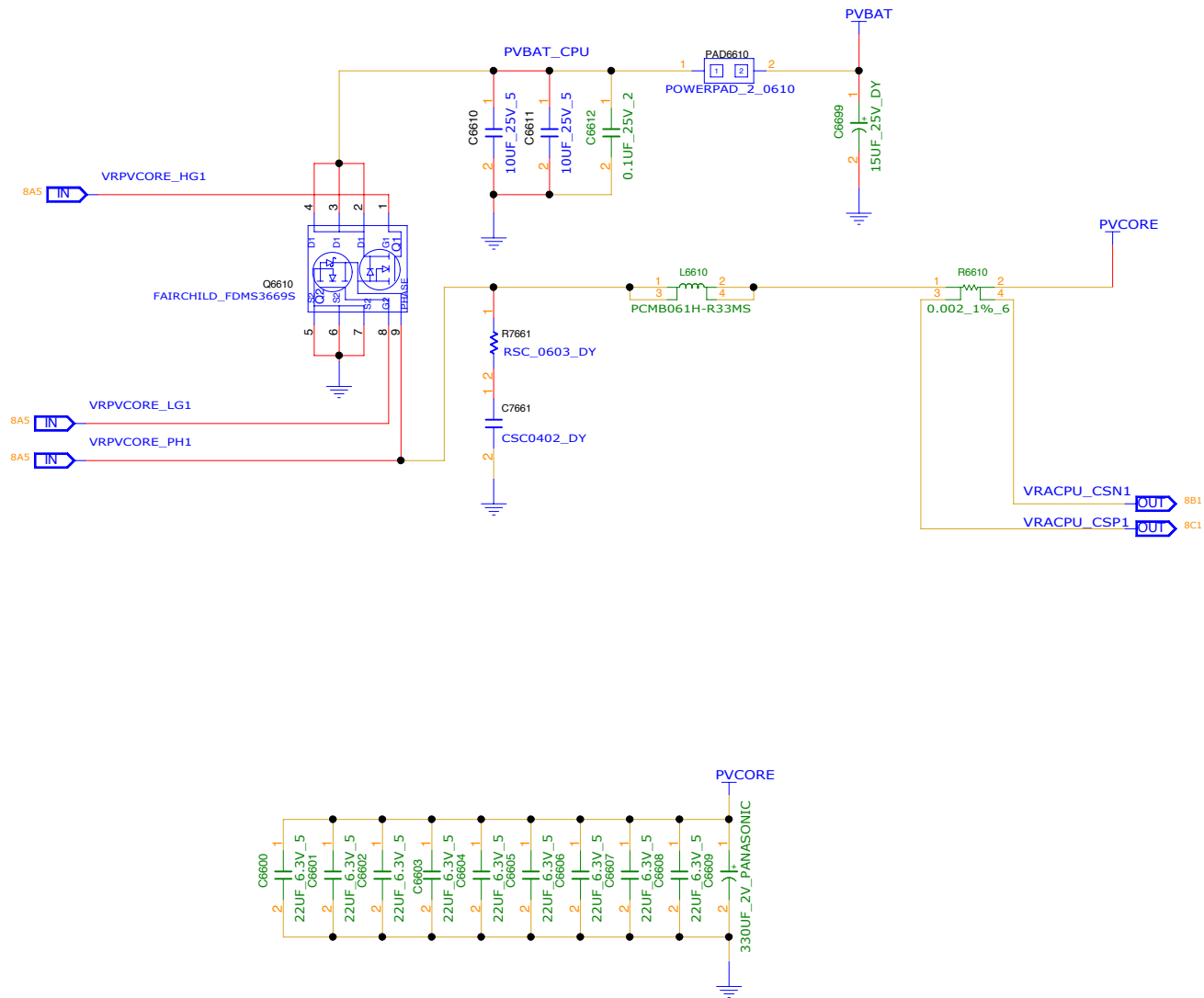
CODE CS

DOC NUMBER 1310xxxxx-0-0

REV X01

SHEET of 6 48

CHANGE by	XXX	DATE	
PCB P/N	<ENG>xx	PCB VER	A71-OCT-2002



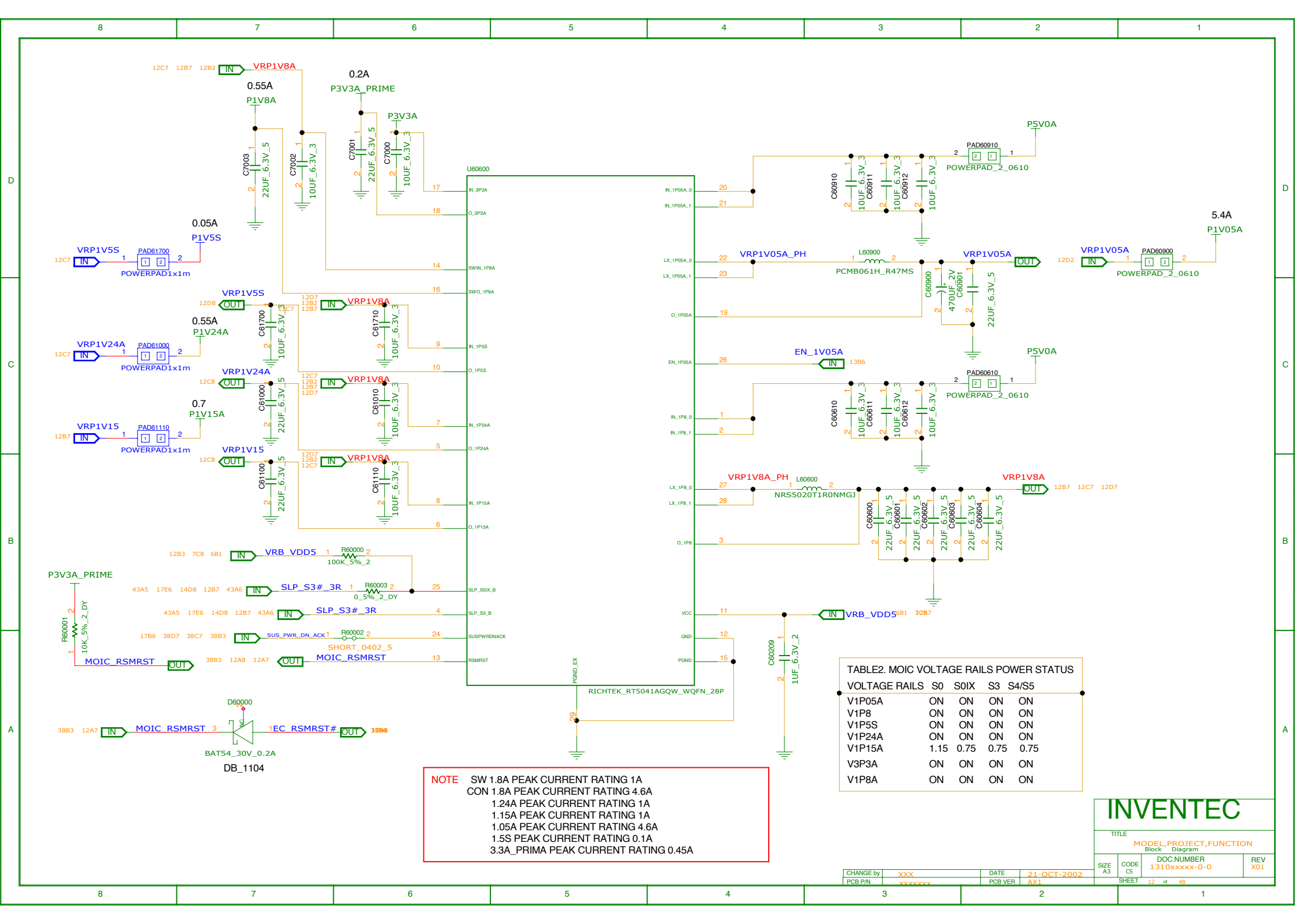
INVENTEC

TITLE
MODEL PROJECT,FUNCTION
VCSSA POWER

SIZE A3 CODE CS DOC NUMBER 1310xxxxx-0-0 REV X01

CHANGE by XXX DATE 21-OCT-2002
PCB P/N xxxxxxxx PCB VER AX1

SHEET 9 of 48



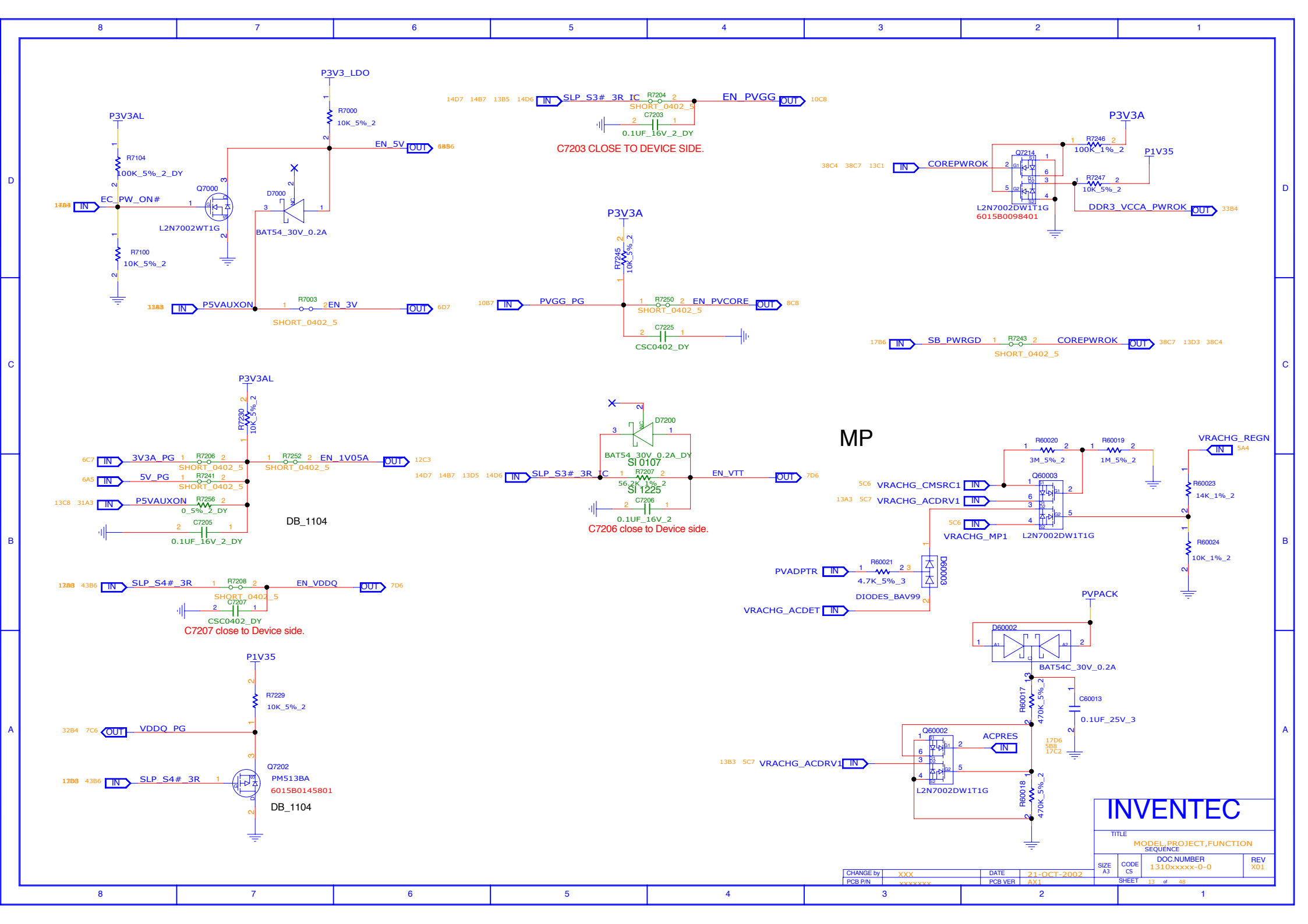
NOTE SW 1.8A PEAK CURRENT RATING 1A
CON 1.8A PEAK CURRENT RATING 4.6A
1.24A PEAK CURRENT RATING 1A
1.15A PEAK CURRENT RATING 1A
1.05A PEAK CURRENT RATING 4.6A
1.5S PEAK CURRENT RATING 0.1A
3.3A_PRIMA PEAK CURRENT RATING 0.45A

TABLE2. MOIC VOLTAGE RAILS POWER STATUS				
VOLTAGE RAILS	S0	S0IX	S3	S4/S5
V1P05A	ON	ON	ON	ON
V1P8	ON	ON	ON	ON
V1P5S	ON	ON	ON	ON
V1P24A	ON	ON	ON	ON
V1P15A	1.15	0.75	0.75	0.75
V3P3A	ON	ON	ON	ON
V1P8A	ON	ON	ON	ON

INVENTEC

TITLE			
MODEL,PROJECT,FUNCTION			
SIZE	CODE	DOC NUMBER	REV
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SHEET 12 of 48			

CHANGE by	XXX	DATE	21-OCT-2002
PCB P/N	xxxxxxx	PCB VER	AX1



C7203 CLOSE TO DEVICE SIDE.

C7206 close to Device side.

C7207 close to Device side.

MP

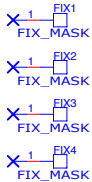
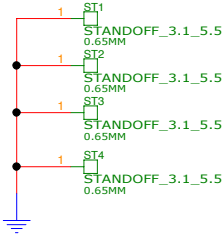
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PCB P/N		xxxxxxx	PCB VER		AX1

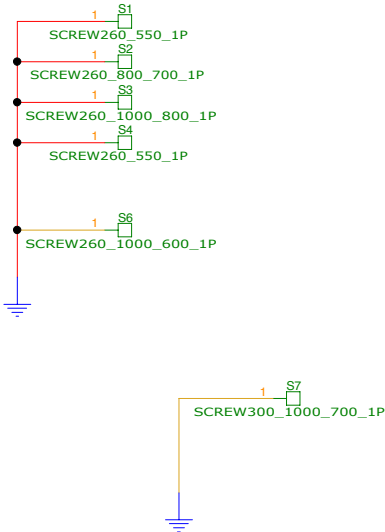
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MODEL PROJECT,FUNCTION			
SEQUENCE			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01
SHEET 13 of 48			

REFERENCE 0~49(PCB SCREW)

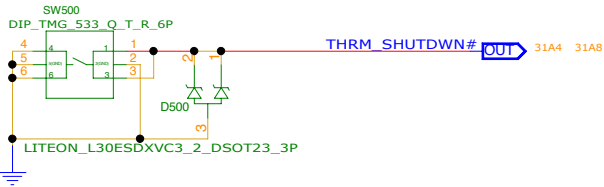
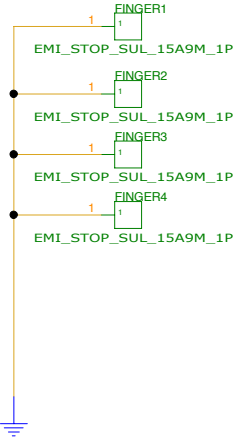
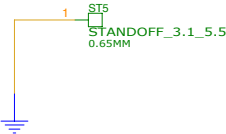
CPU



PCB



3G



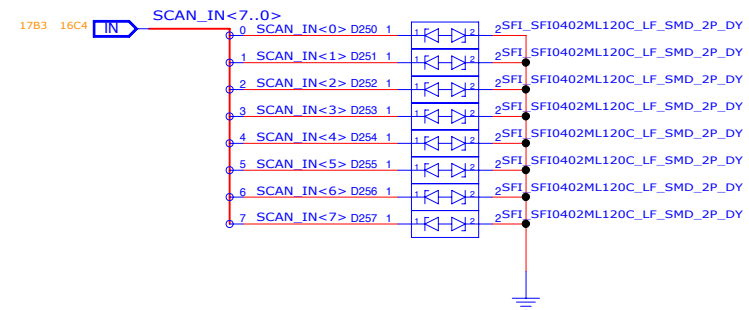
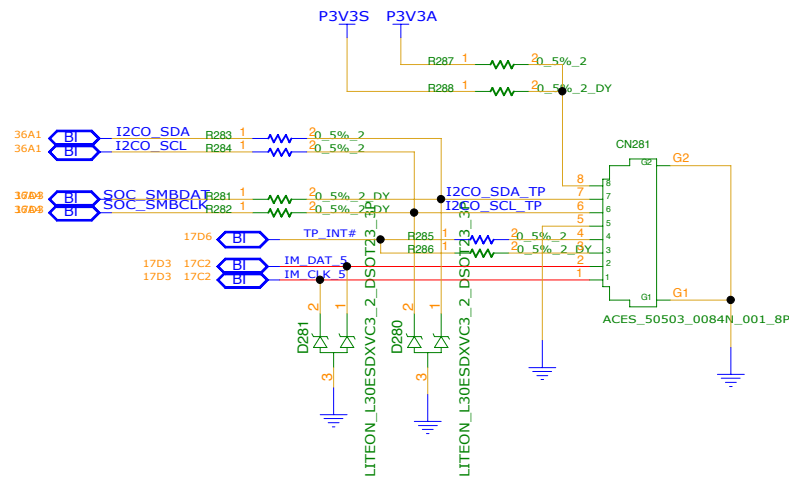
INVENTEC

TITLE
MODEL,PROJECT,FUNCTION
Block Diagram

SIZE A3	CODE CS	DOC NUMBER 1310xxxxx-0-0	REV X01
SHEET	15	of	48

CHANGE by PCB P/N	XXX xxxxxxx	DATE PCB VER	21-OCT-2002 AX1
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KEYBOARD CONN



INVENTEC

TITLE	MODEL, PROJECT, FUNCTION
	Block Diagram

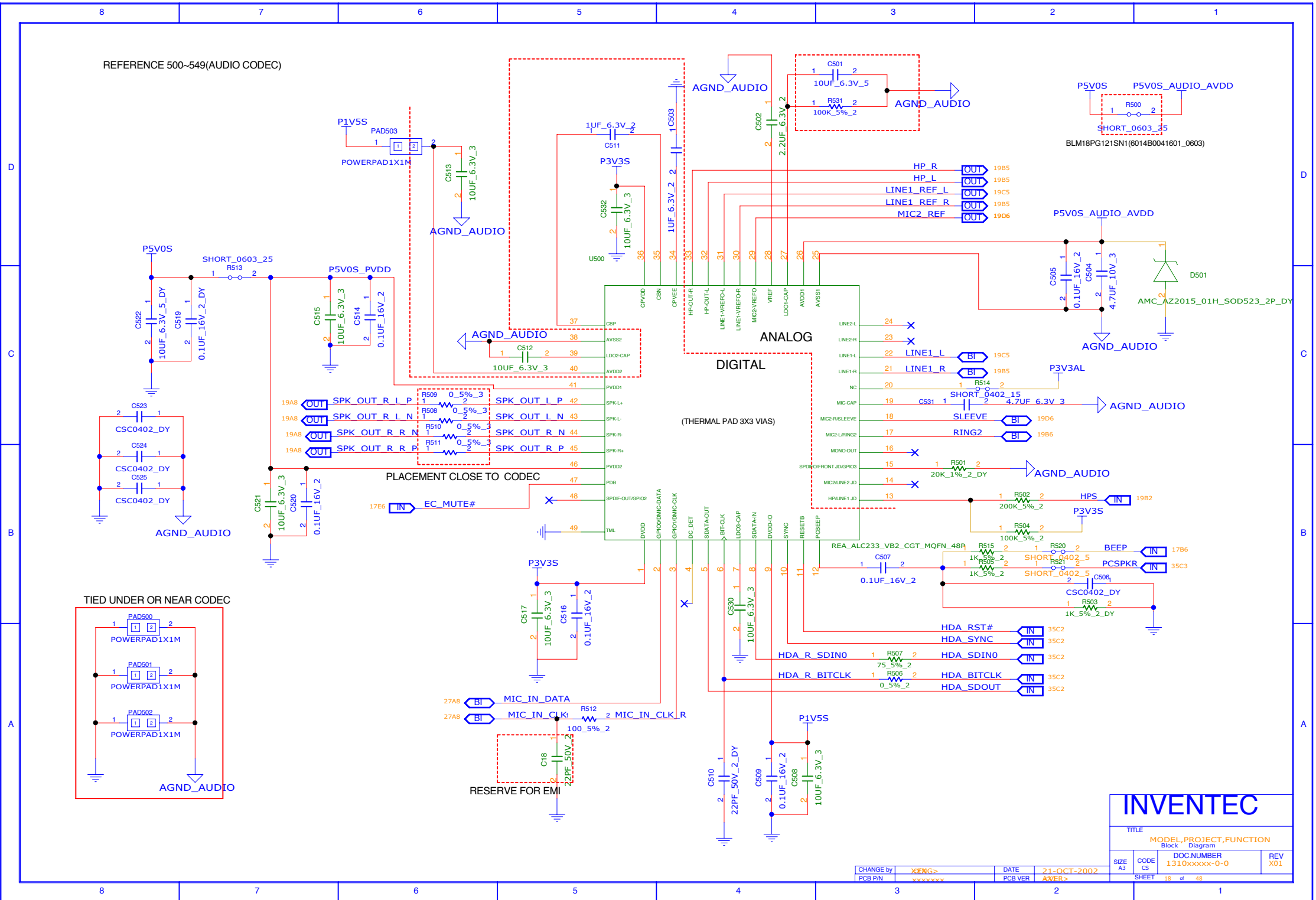
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SHEET		16	of 48

CHANGE by	XXX	DATE	21-OCT-2002
PCB P/N	xxxxxxx	PCB VER	AX1

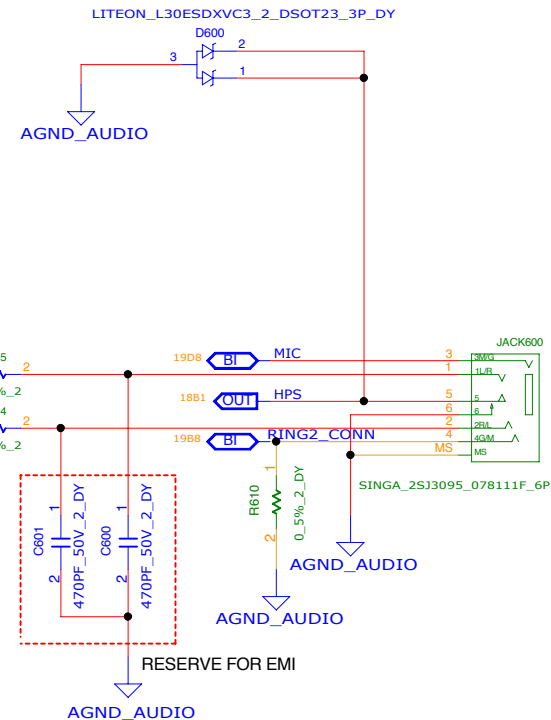
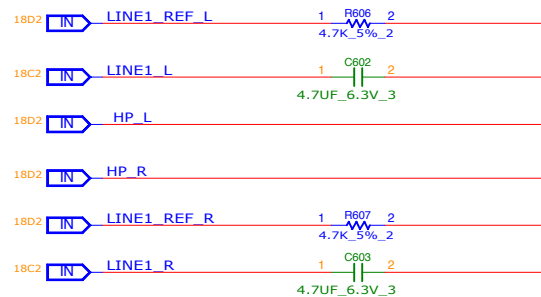
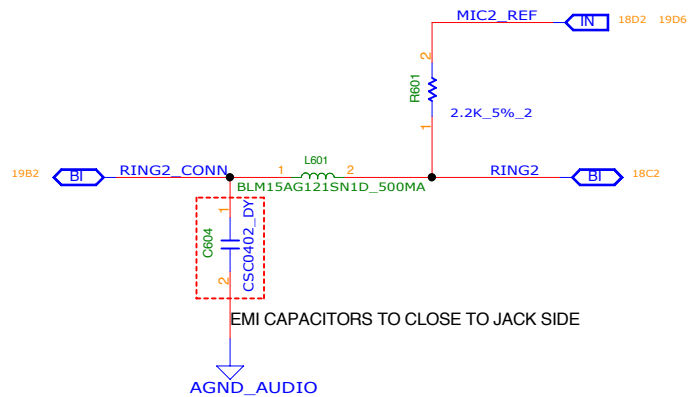
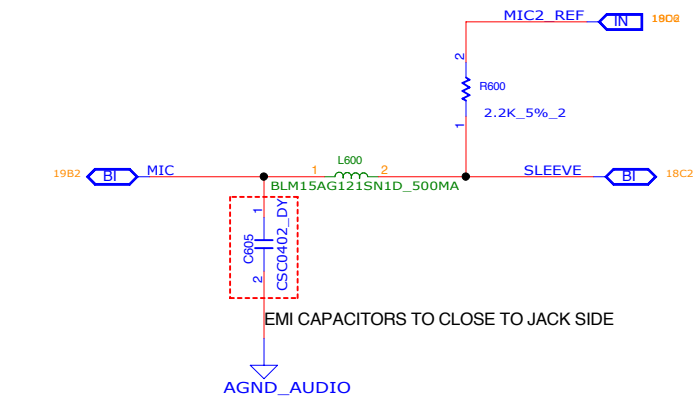
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TITLE			
MODEL,PROJECT,FUNCTION EC			
SIZE C	CODE CS	DOC NUMBER 1310xxxxxx-0-0	REV X01
SHEET of 17 48			

CHANGE b	XXX	DATE		SIZE C	CODE CS	DOC NUMBER 1310xxxxxx-0-0	REV X01
PCB P/N	xxxxxENG	PCB VER 2	00112002		SHEET	of 17	48

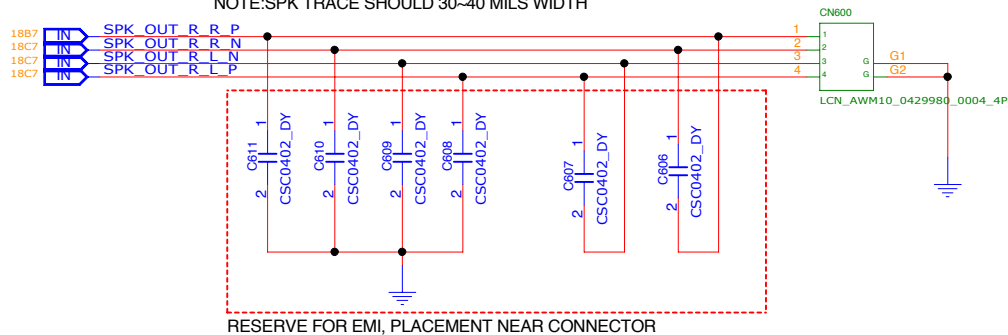


AUDIO JACKS



INTERNAL SPEAKERS

NOTE:SPK TRACE SHOULD 30~40 MILS WIDTH



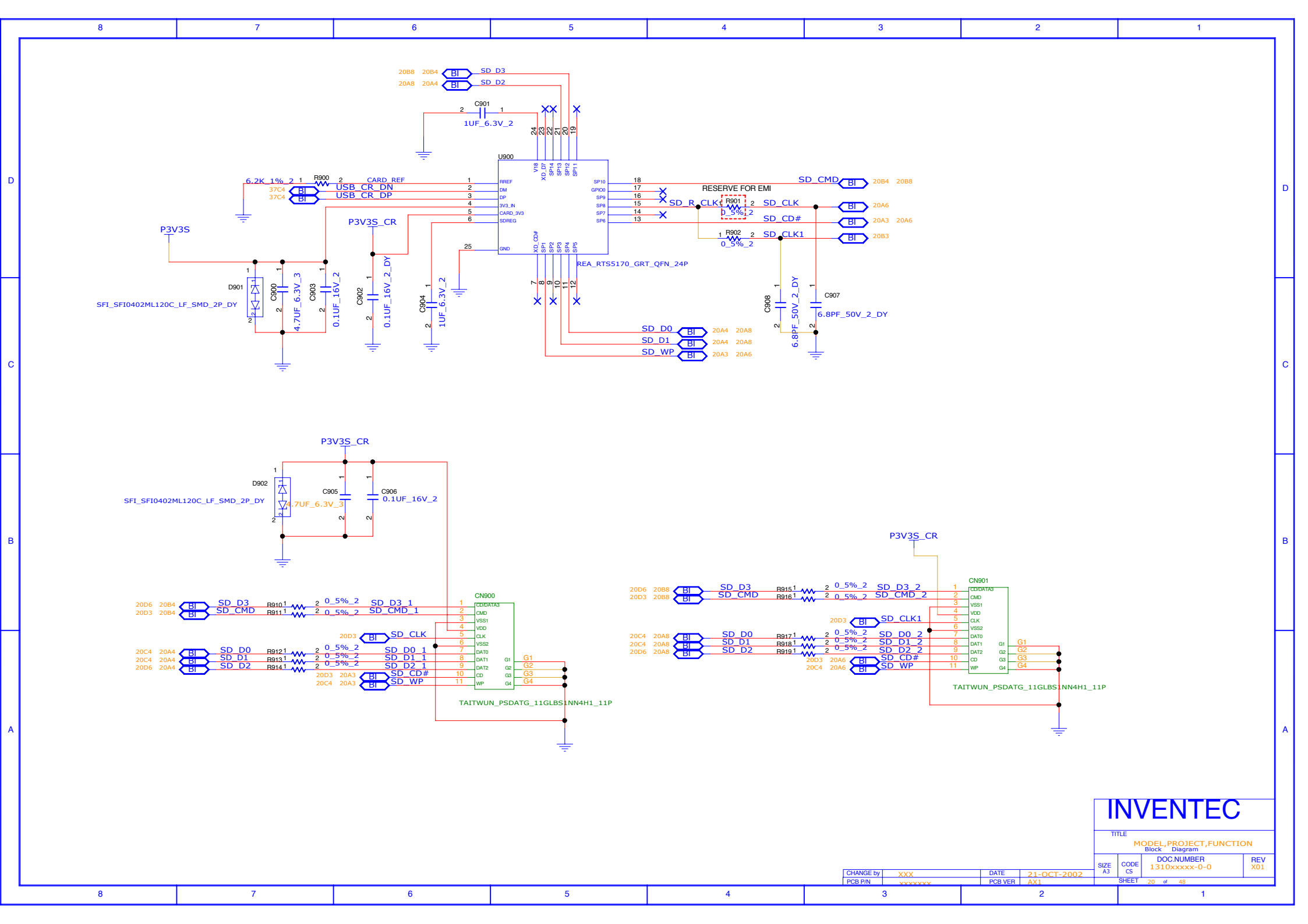
INVENTEC

TITLE
MODEL,PROJECT,FUNCTION

SIZE A3 CODE CS DOC NUMBER 1310xxxxx-0-0 REV X01

CHANGE by XXX DATE 21-OCT-2002
PCB P/N xxxxxxxx PCB VER AX1

SHEET 19 of 48



INVENTEC

TITLE
MODEL,PROJECT,FUNCTION
Block Diagram

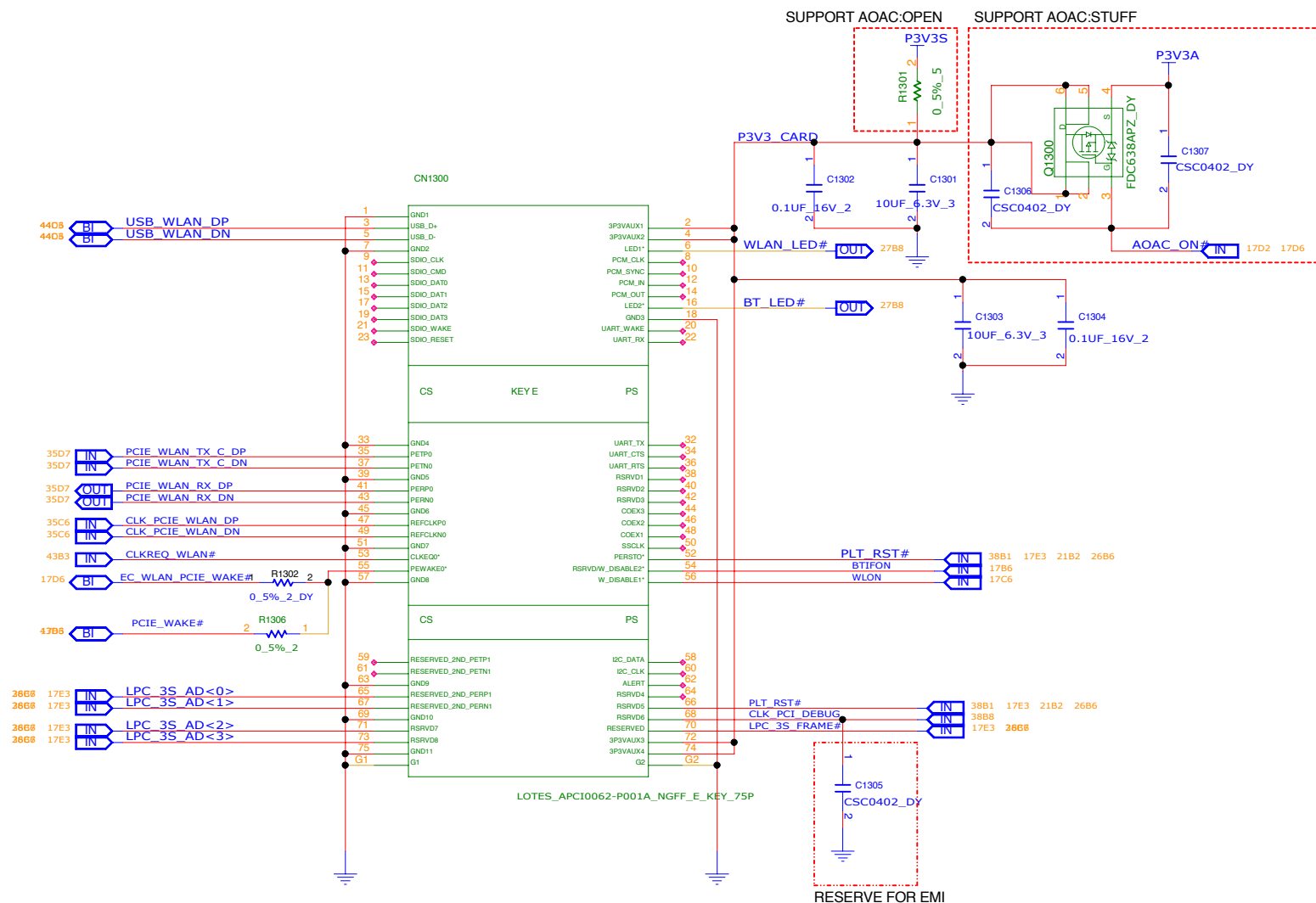
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CHANGE by XXX DATE 21-OCT-2002
PCB P/N xxxxxxxx PCB VER AX1

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WIFI AND DEBUG CARD CO-LAYOUT CONNECTOR

REF: REFERENCE: 1310-0000-0000-0000



INVENTEC				
TITLE				
MODEL,PROJECT,FUNCTION				
Block Diagram				
SIZE	CODE	DOC NUMBER	REV	
A3	CS	1310xxxxx-0-0	X01	
SHEET 21 of 48				

CHANGE by	XXX	DATE	21-OCT-2002
PCB P/N	xxxxxxx	PCB VER	AX1

REFERENCE 1400~1499(3G)

3G

INVENTEC

TITLE

MODEL,PROJECT,FUNCTION	
Block	Diagram

SIZE
A2

CODE
CE

DOC.NUMBER
1310xxxxx-0-0

REV
X01

SHEET 22 of 48

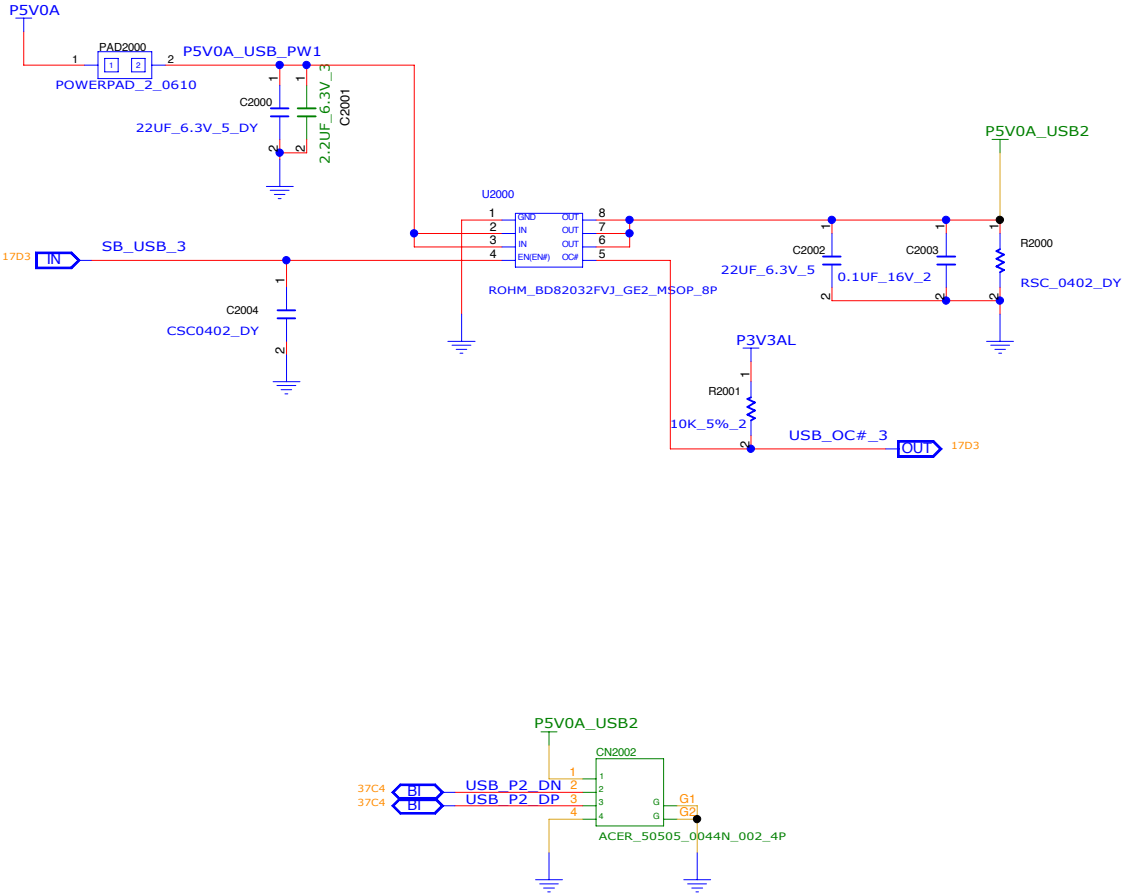
CHANGE by
PCB P/N

XXX
XXXXXXXXXX

DATE	21-OCT-2002
PCB VER	AX1

DATE	21-0
PCB VER	AX1

REFERENCE 2000~2099(USB)

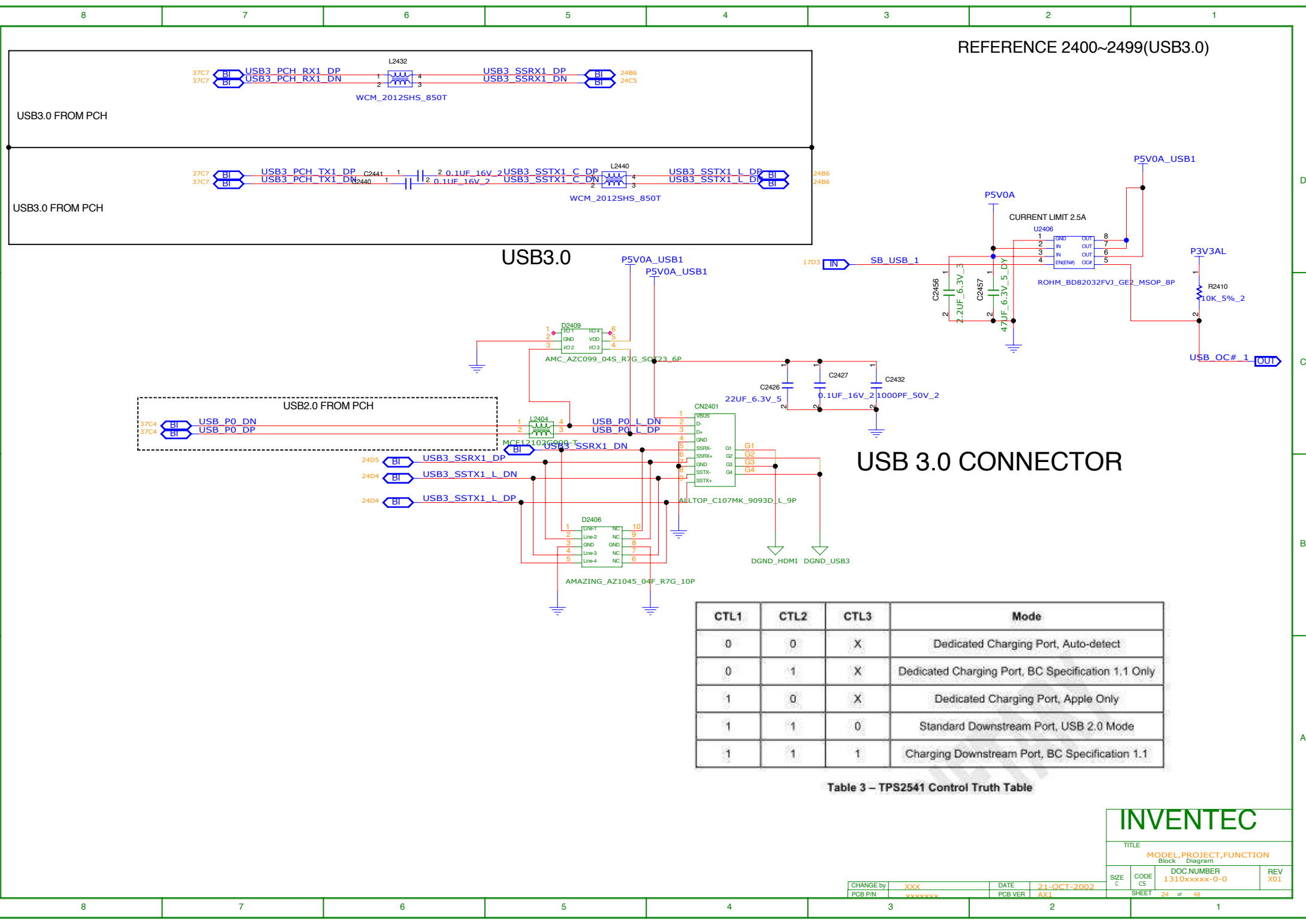


INVENTEC

TITLE
MODEL,PROJECT,FUNCTION
Block Diagram

SIZE A3	CODE CS	DOC NUMBER 1310xxxxx-0-0	REV X01
SHEET		23 of 48	

CHANGE by PCB P/N	XXX xxxxxxx	DATE PCB VER	21-OCT-2002 AX1
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USB 3.0 CONNECTOR

CTL1	CTL2	CTL3	Mode
0	0	X	Dedicated Charging Port, Auto-detect
0	1	X	Dedicated Charging Port, BC Specification 1,1 Only
1	0	X	Dedicated Charging Port, Apple Only
1	1	0	Standard Downstream Port, USB 2.0 Mode
1	1	1	Charging Downstream Port, BC Specification 1.1

Table 3 – TPS2541 Control Truth Table

INVENTEC

TITLE
MODEL,PROJECT,FUNCTION

SIZE CODE DOC NUMBER
C CS 1310xxxx-0-0

SHEET 24 of 48

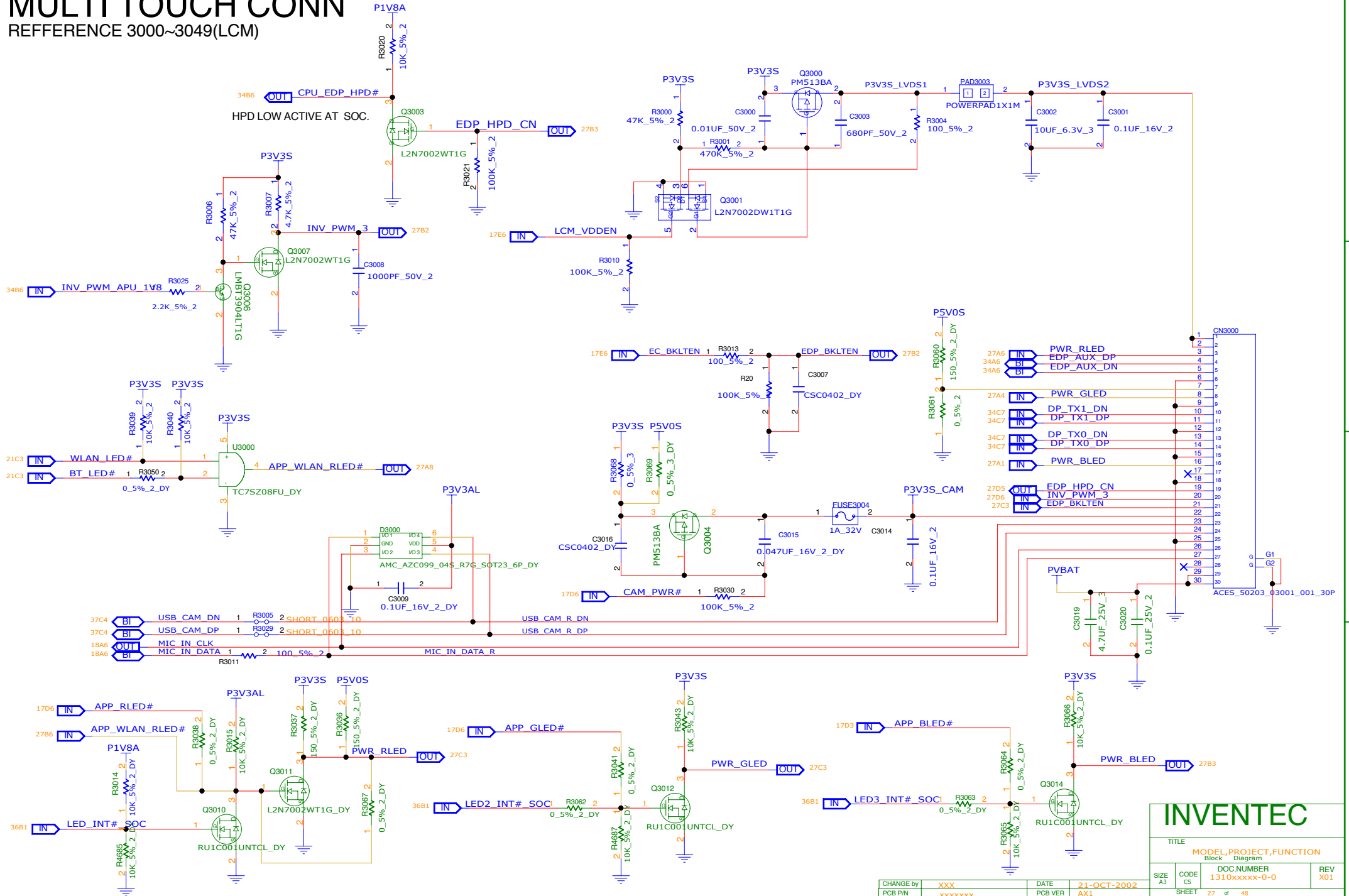
CHANGE by XXX
PCB P/N xxxxxxxx

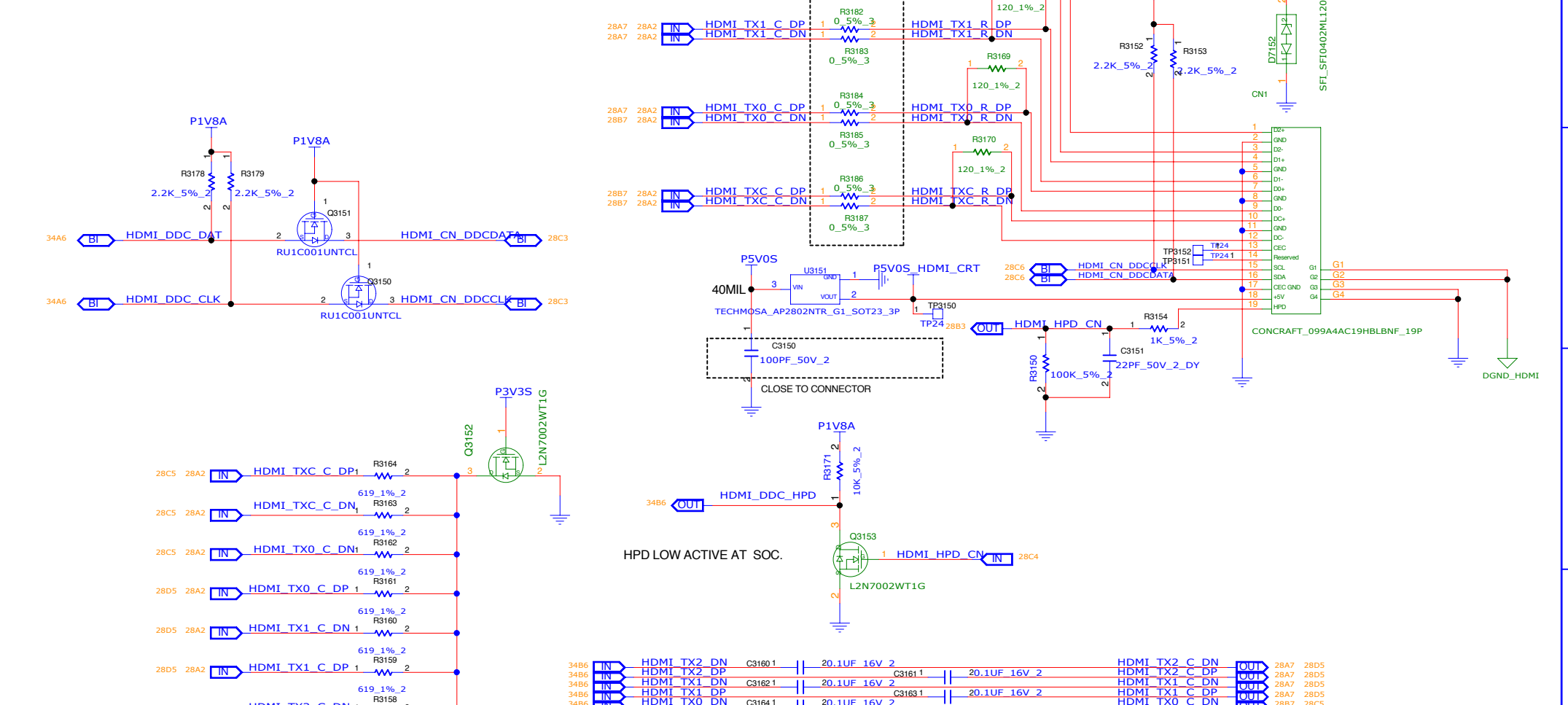
DATE 21-OCT-2002
PCB VER AX1

REV X01

MULTI TOUCH CONN

REFERENCE 3000~3049(LCM)





TITLE	MODEL,PROJECT,FUNCTION
	HDMI

CHANGE by	XXX	DATE	
PCB P/N	ENG-001	PCB VER	A11-OCT-2002

F



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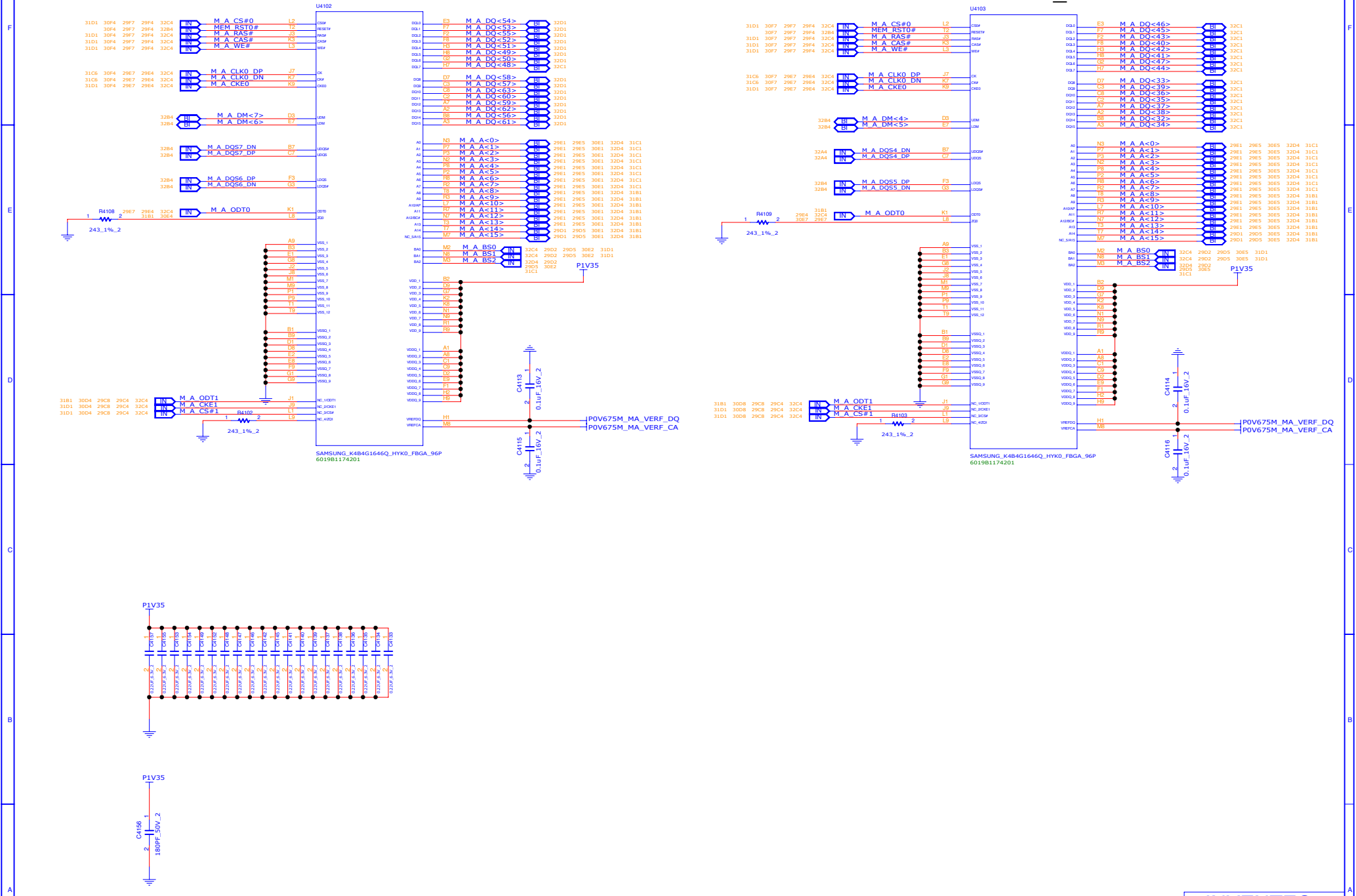


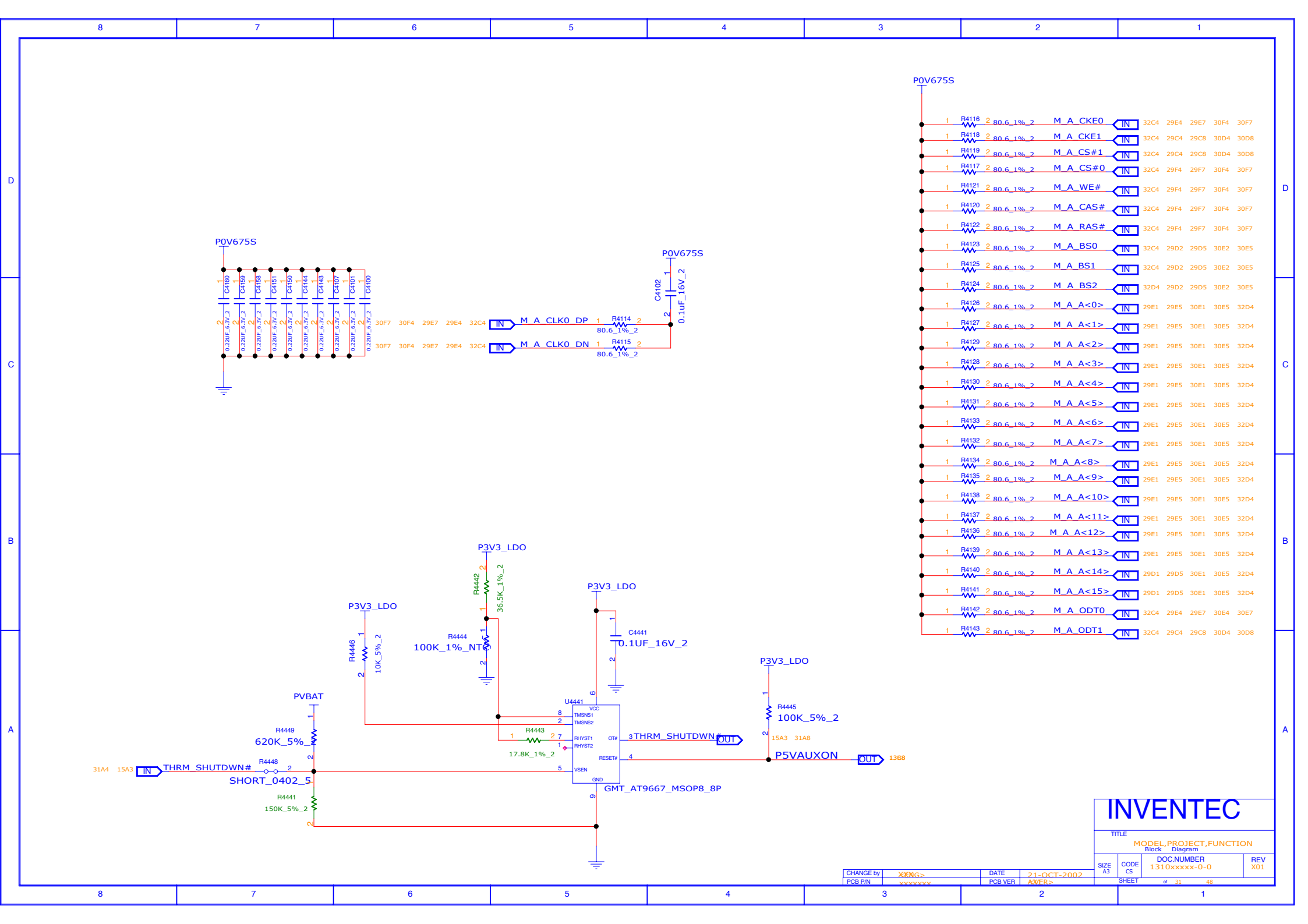
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CHANGE BY	KENG>	DATE	21-OCT-2002	SIZE	A3	CODE	CS	DOC NUMBER	1310XXXX-0-0	REV	X01
PCB P/N	XXXXXX	PCB VER	AXER>	SHEET of 29 45							

DDR3L_2

DDR3L_3





INVENTEC

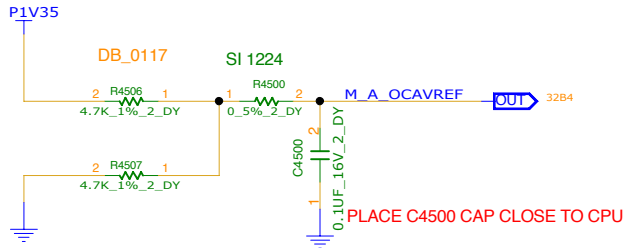
TITLE
MODEL,PROJECT,FUNCTION
Block Diagram

SIZE A3	CODE CS	DOC NUMBER 1310xxxxx-0-0	REV X01
SHEET of 31		48	

CHANGE by PCB P/N	XXXXXX	DATE PCB VER	21-OCT-2002 AWEF
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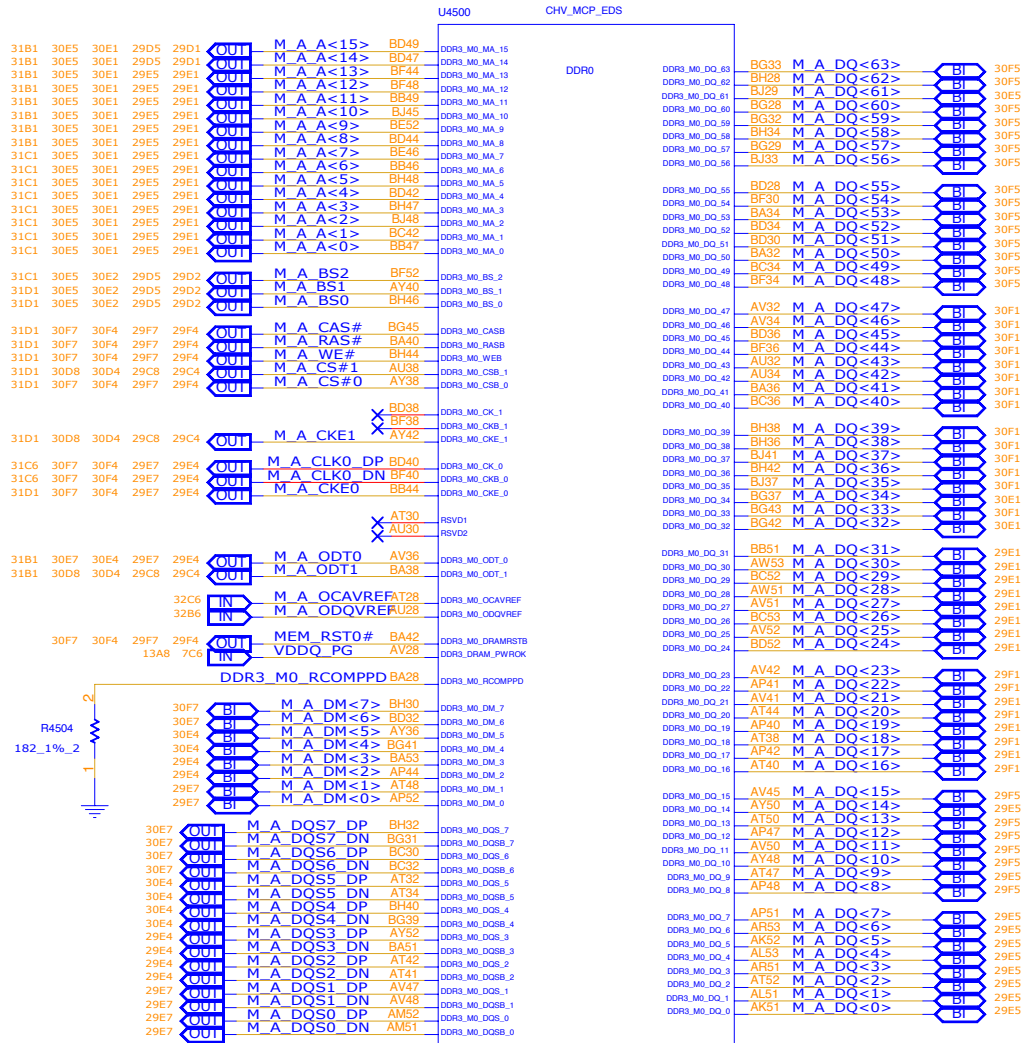
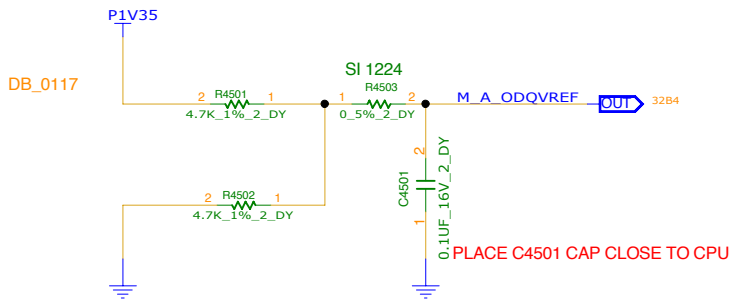
NOTE

PLACE TWO 4.7K RESISTORS CLOSE TO CPU PINS ON M_VREF
ROUTE THE VREF POWER SIGNALS WITH THICK TRACES



NOTE

PLACE TWO 4.7K RESISTORS CLOSE TO CPU PINS ON M_VREF
ROUTE THE VREF POWER SIGNALS WITH THICK TRACES



U4500 CHV_MCP_EDS

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DDR3

DDR3

DDR3

DDR3

DDR3

DDR3

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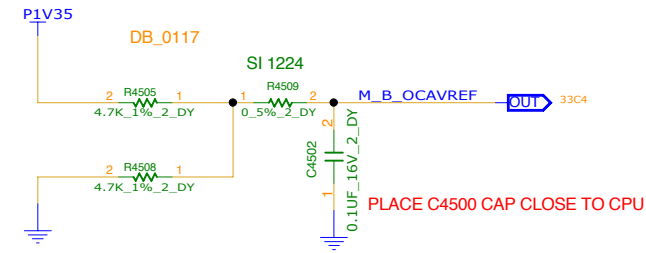
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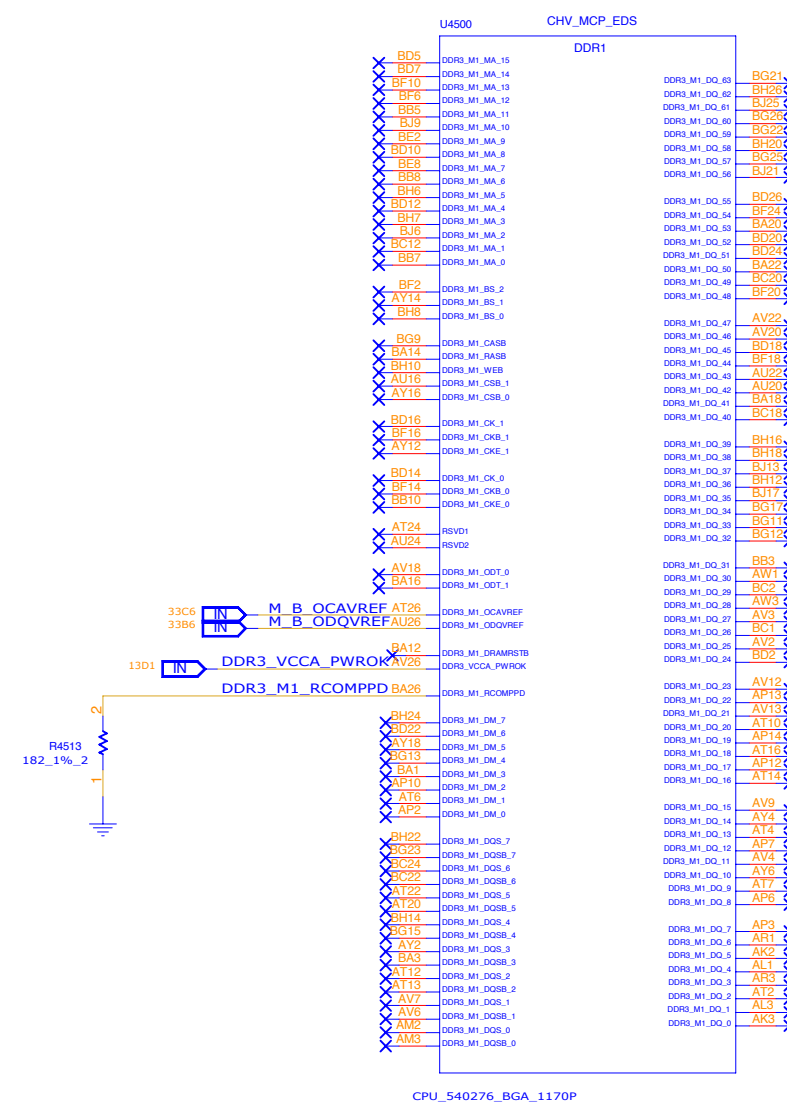
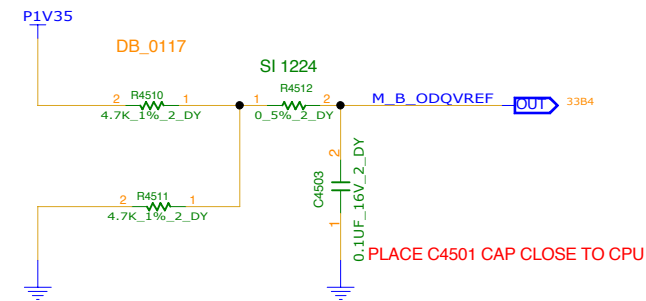
DDR3

DDR3</

NOTE
PLACE TWO 4.7K RESISTORS CLOSE TO CPU PINS ON M_VREF
ROUTE THE VREF POWER SIGNALS WITH THICK TRACES



NOTE
PLACE TWO 4.7K RESISTORS CLOSE TO CPU PINS ON M_VREF
ROUTE THE VREF POWER SIGNALS WITH THICK TRACES





INVENTEC

SIZE A3	CODE CS	DOC.NUMBER 1310xxxxx-0-0	F X
SHEET		34 of 48	

CHANGE by	XXX	DATE	21-OCT-2002	SIZE	A3	CODE	CS	1310xxxxx-0-0	X01
PCB P/N	xxxxxxx	PCB VER	AX1	SHEET	34	of	48		

WLAN

SPI FLASH

TRUTH TABLE

S	OE	FUNCTION
X	H	Disconnect
L	L	D = 1D
H	L	D = 2D

INVENTEC

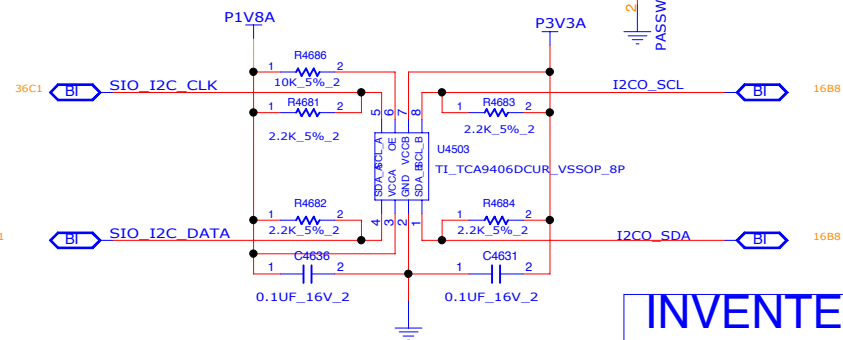
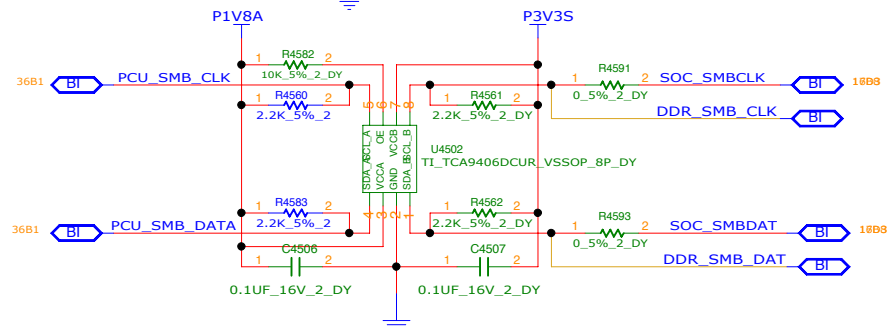
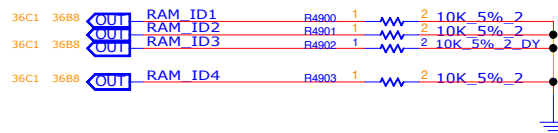
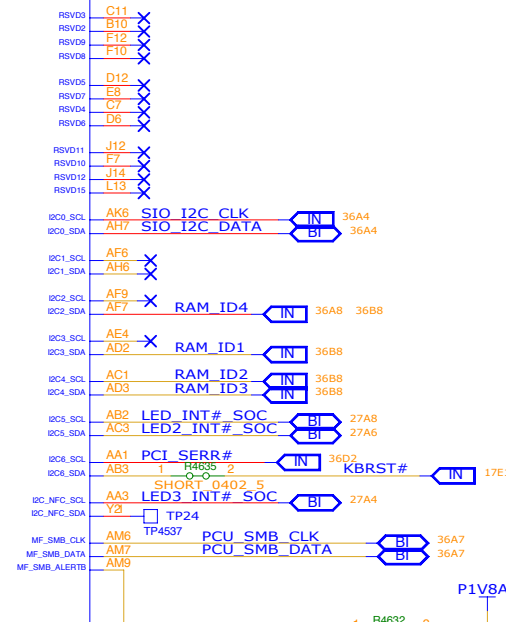
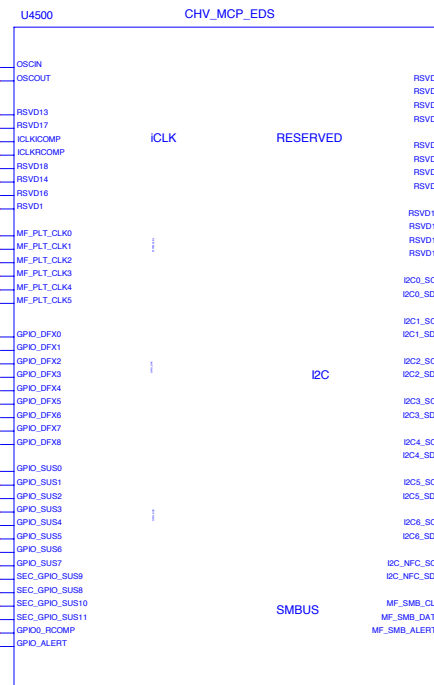
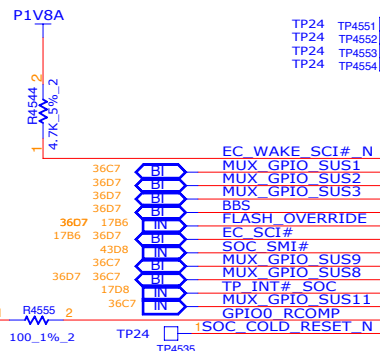
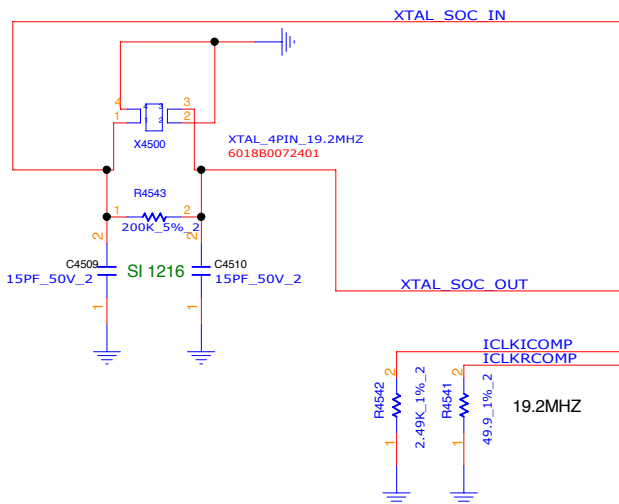
TITLE
MODEL,PROJECT,FUNCTION
Block Diagram

SIZE A3	CODE CS	DOC NUMBER 1310xxxxx-0-0	REV X01
SHEET	35	of 48	

CHANGE by PCB P/N	XXX xxxxxxx	DATE PCB VER	21-OCT-2002 AX1
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PLEASE REFERENCE TO CRB PAGE23

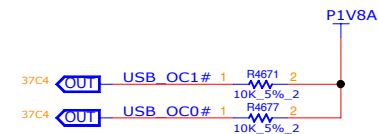
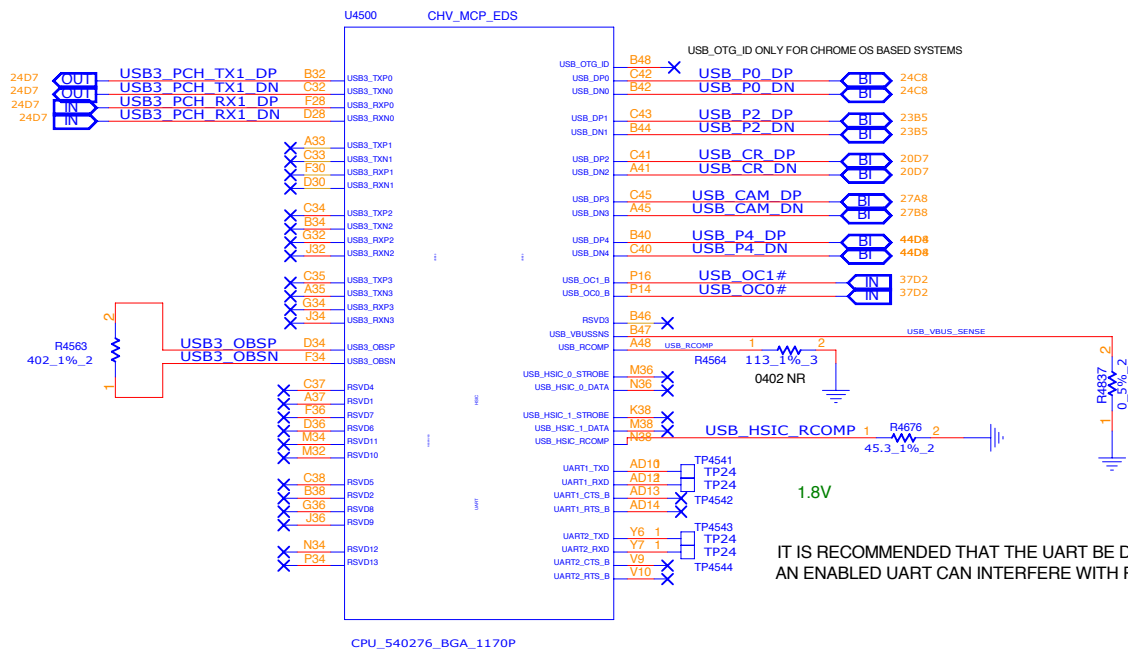
Register Name	Address	Value
FLASH_OVERRIDE	17B6	36B3
EC_SCI#	36B5	17B6
MUX_GPIO_SUS2	36B5	
BBS	36B5	
PD_DSI_DISABLE		
MUX_GPIO_SUS3	36B5	
MUX_GPIO_SUS8	36B5	36C7
MUX_GPIO_SUS9	36B5	36C7
MUX_GPIO_SUS1	36B5	
FLASH_OVERRIDE	17B6	36B3



TITLE		MODEL,PROJECT,FUNCTION	
		Block	Diagram
		DOCUMENT NUMBER	

CHANGE by	XXX	DATE		SHEET	A3	CS	1310xxxxx-U-0	X01
PCB P/N	<ENG>xxx	PCB VER	Ax1-OCT-2002	SHEET			of 36 48	

USB3.0

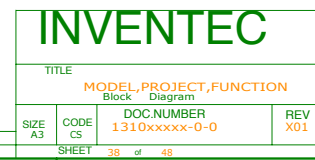


INVENTEC

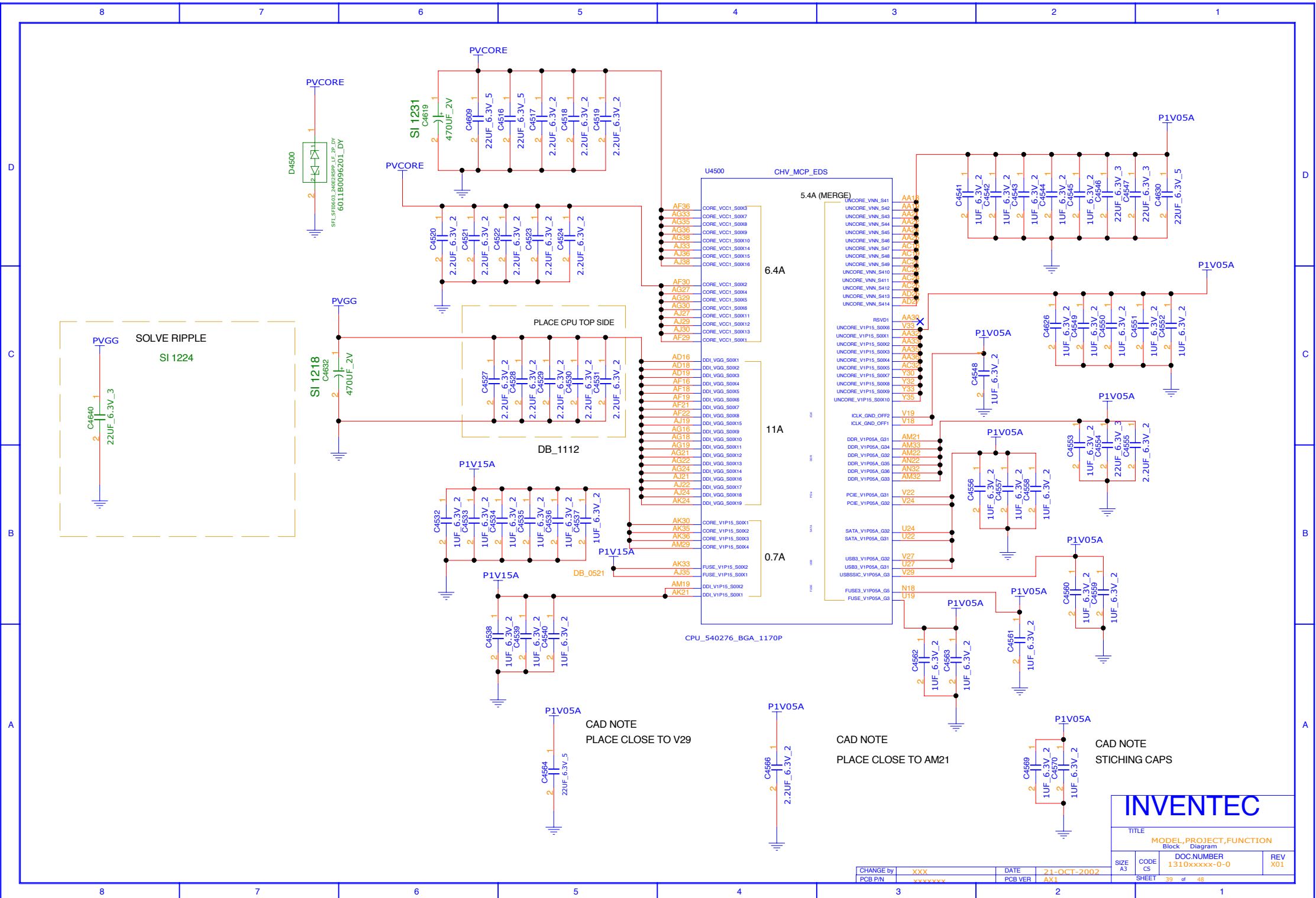
TITLE
MODEL,PROJECT,FUNCTION
Block Diagram

SIZE A3	CODE CS	DOC NUMBER 1310xxxxx-0-0	REV X01
SHEET		37 of 48	

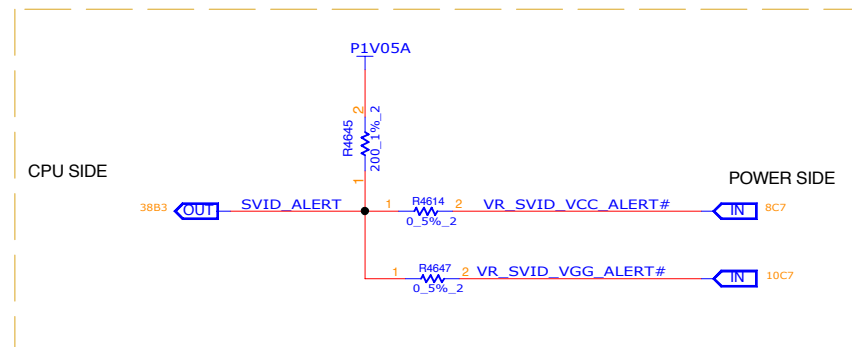
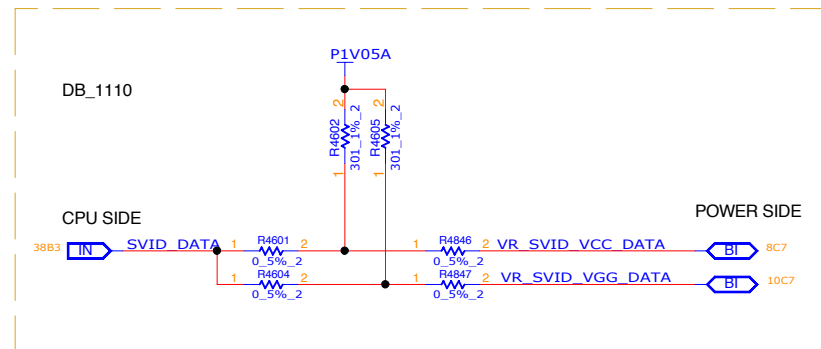
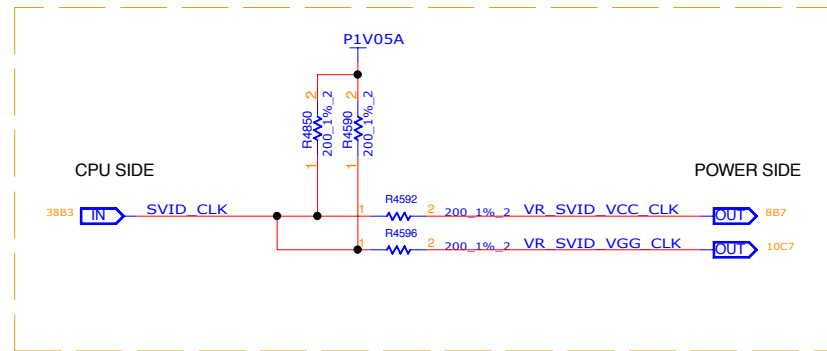
CHANGE by	XXX	DATE	21-OCT-2002
PCB P/N	xxxxxxx	PCB VER	AX1



CHANGE by	XXX	DATE	21-OCT-2002	SIZE	A3	CODE	CS	1310xxxxx-0-0	X01
PCB P/N	xxxxxxx	PCB VER	AX1			SHEET	38	of	48



SVID



INVENTEC

TITLE
MODEL, PROJECT, FUNCTION
Block Diagram

SIZE A3 CODE CS DOC NUMBER 1310xxxxx-0-0 REV X01

CHANGE by XXX DATE 21-OCT-2002
PCB P/N xxxxxxxx PCB VER AX1

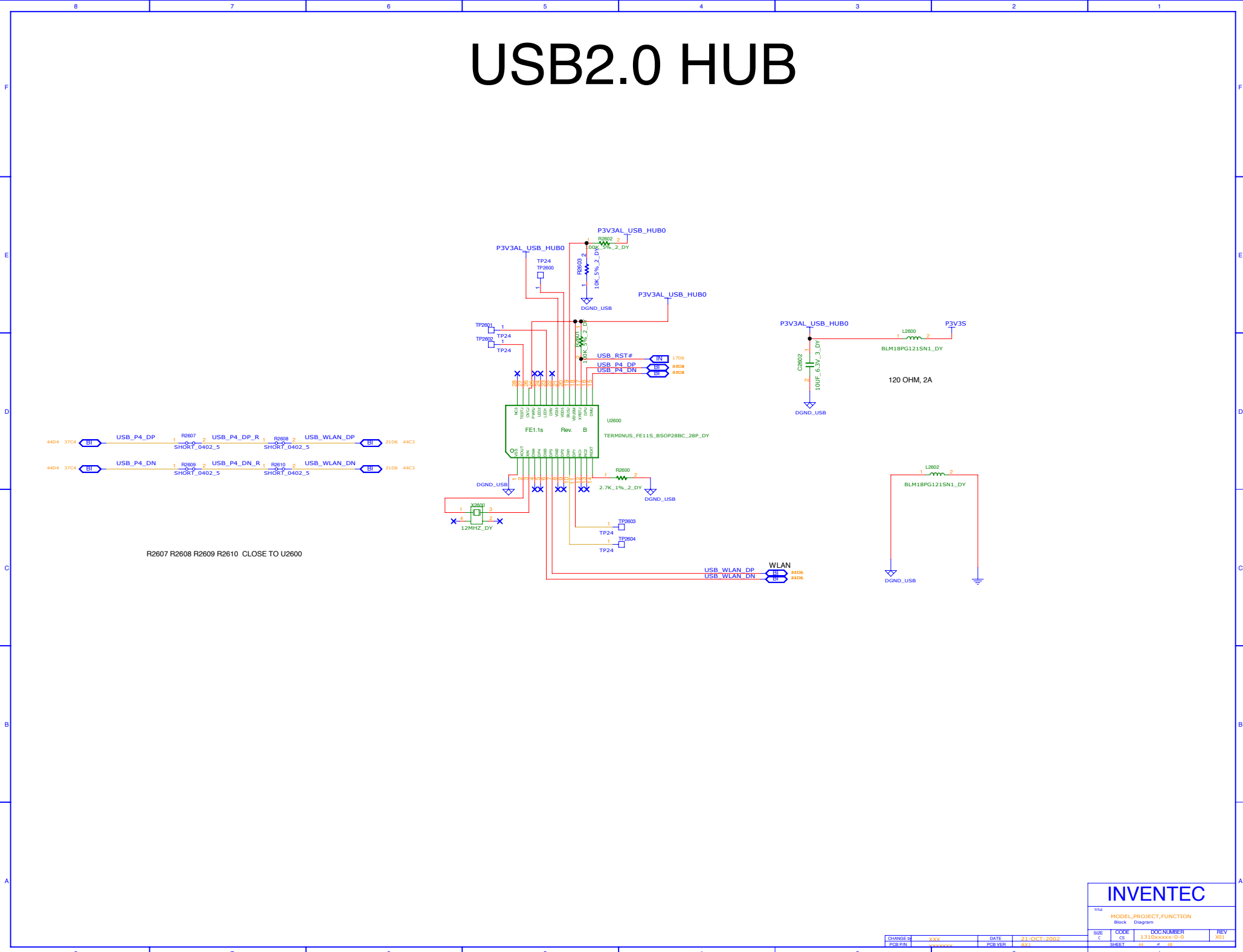
SHEET 42 of 48

USB2.0 HUB

R2607 R2608 R2609 R2610 CLOSE TO U2600

CHANGE	DATE	BY	REV
1	21-OCT-2002	AX	X01

SHEET	64	REV	X01
INVENTEC			

[illegible]

USB2.0 HUB

44D4 37C4 BI USB_P4_DP 1 R2607 USB_P4_DP_R 1 R2608 USB_WLAN_DP BI 21D6 44C3
SHORT_0402_5 SHORT_0402_5

44D4 37C4 BI USB_P4_DN 1 R2609 USB_P4_DN_R 1 R2610 USB_WLAN_DN BI 21D6 44C3
SHORT_0402_5 SHORT_0402_5

R2607 R2608 R2609 R2610 CLOSE TO U2600

TP241 TP24 TP240 TP24

P3V3AL_USB_HUB0

P3V3AL_USB_HUB0

P3V3AL_USB_HUB0

USB_RST# 17D6
USB_P4_DP 88D8
USB_P4_DN 88D8

FE1.1s Rev. B U2600
TERMINUS_FE11S_BSOP28BC_28P_DY

DGND_USB

12MHZ_DY

TP2603 TP24 TP2604 TP24

USB_WLAN_DP WLAN 44D6
USB_WLAN_DN 44D6

P3V3AL_USB_HUB0

C2602 10UF 6.3V 3_DY

DGND_USB

L2600 120 OHM, 2A

P3V3S

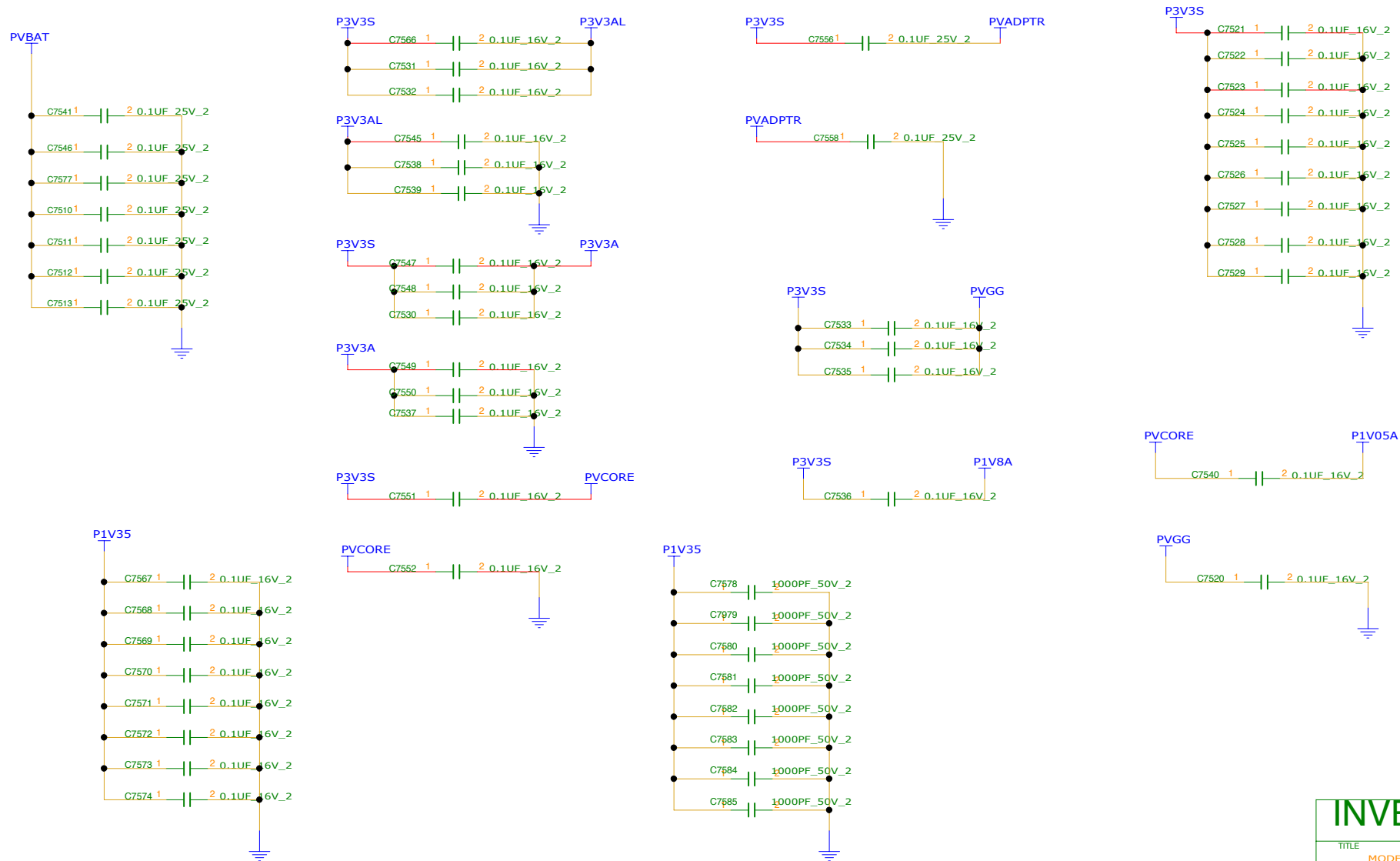
BLM18PG121SN1_DY

DGND_USB

INVENTEC			
TITLE			
MODEL, PROJECT, FUNCTION			
Block Diagram			
SUB C	CODE CS	DOC NUMBER	REV
CHANGE D	xxx	DATE	21-OCT-2002
PDB PIN	xxxxxx	PDB VER	AX1
SHEET	61	#	68

[illegible][illegible]

EMI



INVENTEC

TITLE
MODEL,PROJECT,FUNCTION
Block Diagram

SIZE
A3

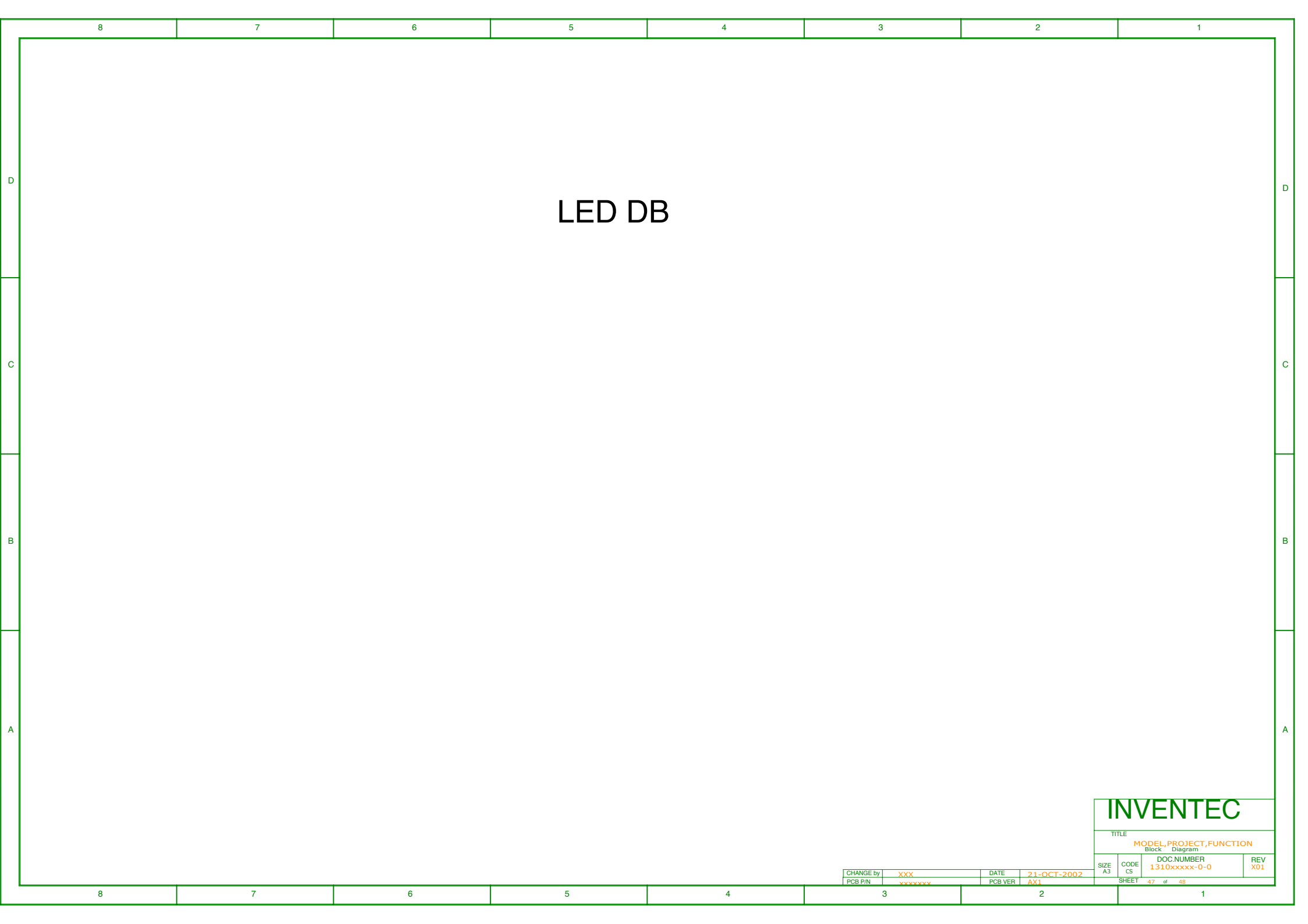
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CS

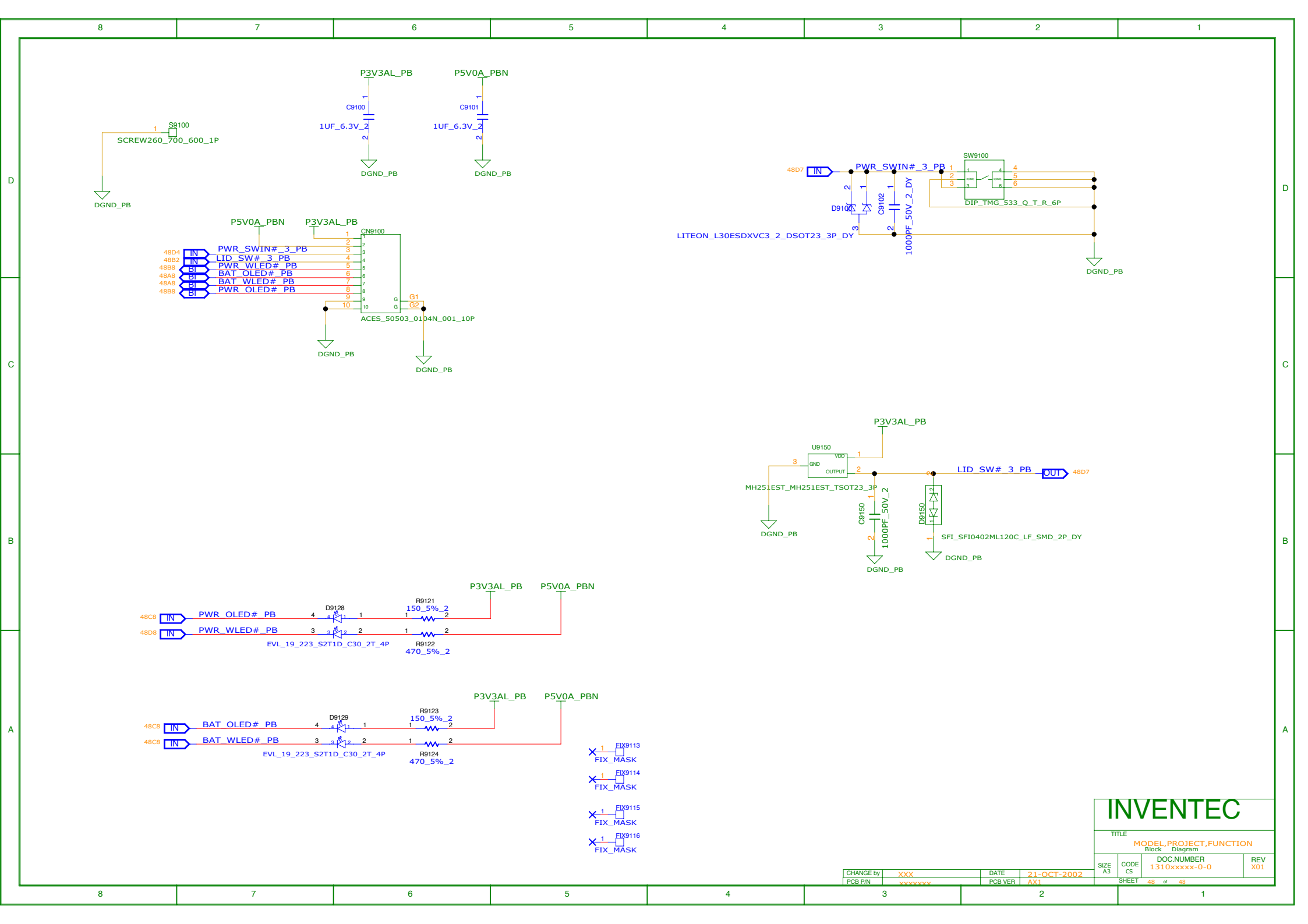
DOC NUMBER
1310xxxxx-0-0

REV
X01

SHEET
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CHANGE by	XXX	DATE	21-OCT-2002
PCB P/N	xxxxxxx	PCB VER	AX1





INVENTEC

TITLE
MODEL,PROJECT,FUNCTION
Block Diagram

SIZE A3	CODE CS	DOC NUMBER 1310xxxxx-0-0	REV X01
SHEET		48 of 48	

CHANGE by PCB P/N	XXX xxxxxxx	DATE PCB VER	21-OCT-2002 AX1
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